

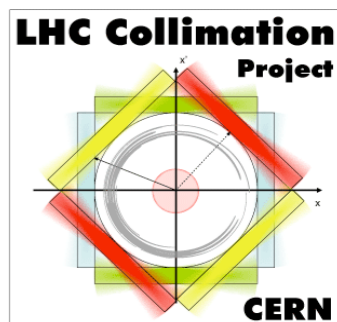
Collimator Control Application from the Control Room

Stefano Redaelli, AB-OP

for the COCOST team

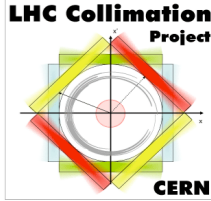
Acknowledgments: M. Lamont, J. Wenninger, SPS-OP crew,

BLM team, ABP collimation team





Overview of my talk

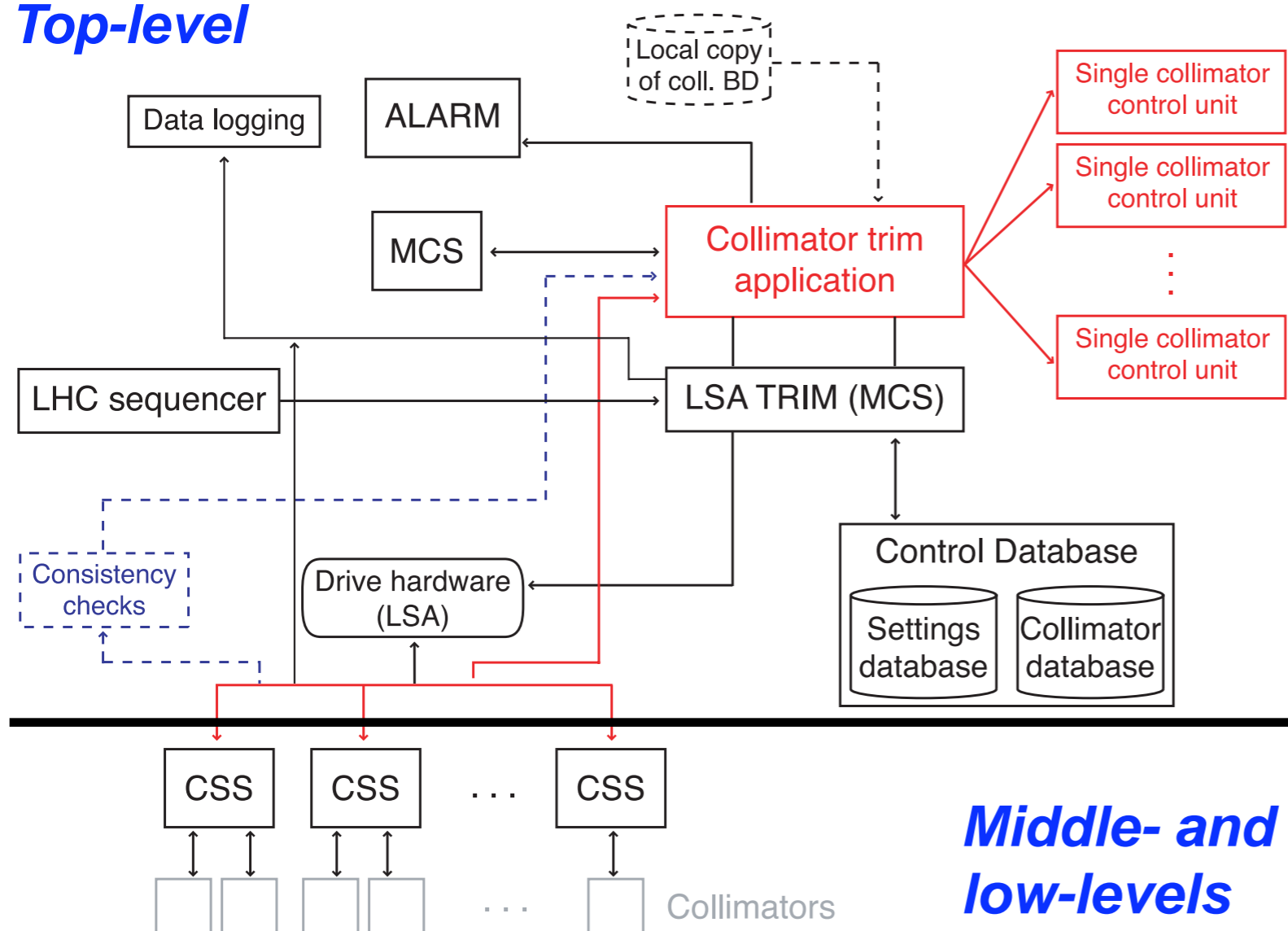


- 1. Introduction**
- 2. Implementation of LHC software**
- 3. Control through the LSA TRIM**
- 4. Performance issues**
- 5. Conclusions**

Architecture of top-level collimator controls

(Eng. Spec. May 2006, to be published)

Top-level



Beam tests 2006: focus on **Single Collimator Control**, discrete settings of absolute positions (*not yet time-functions*)

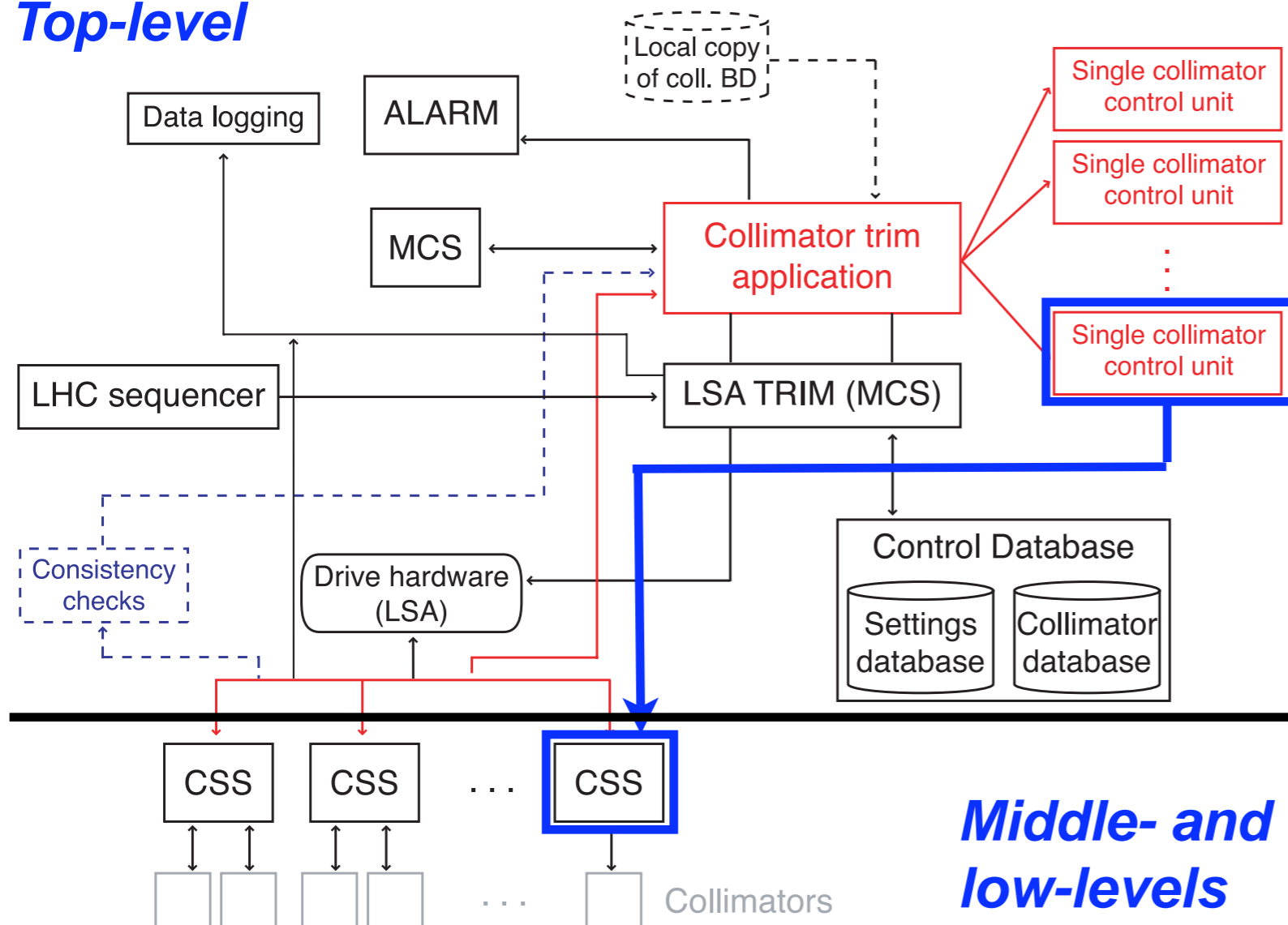
... a little part of the overall architecture BUT crucial for the LHC!

Major role in the system commissioning: collimation tuning will rely on manual **beam-based alignment** until we gain experience and setup automatic procedures

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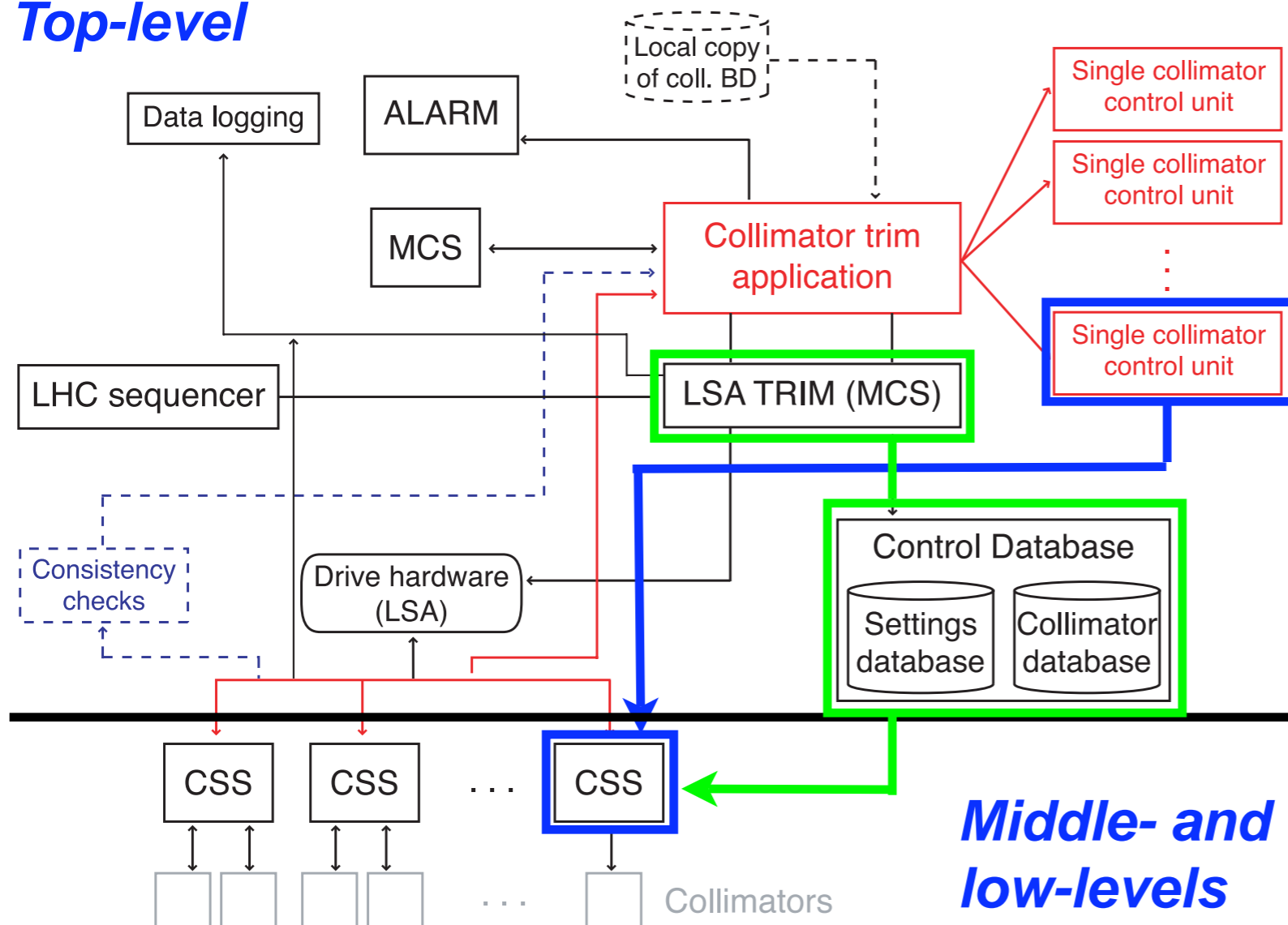
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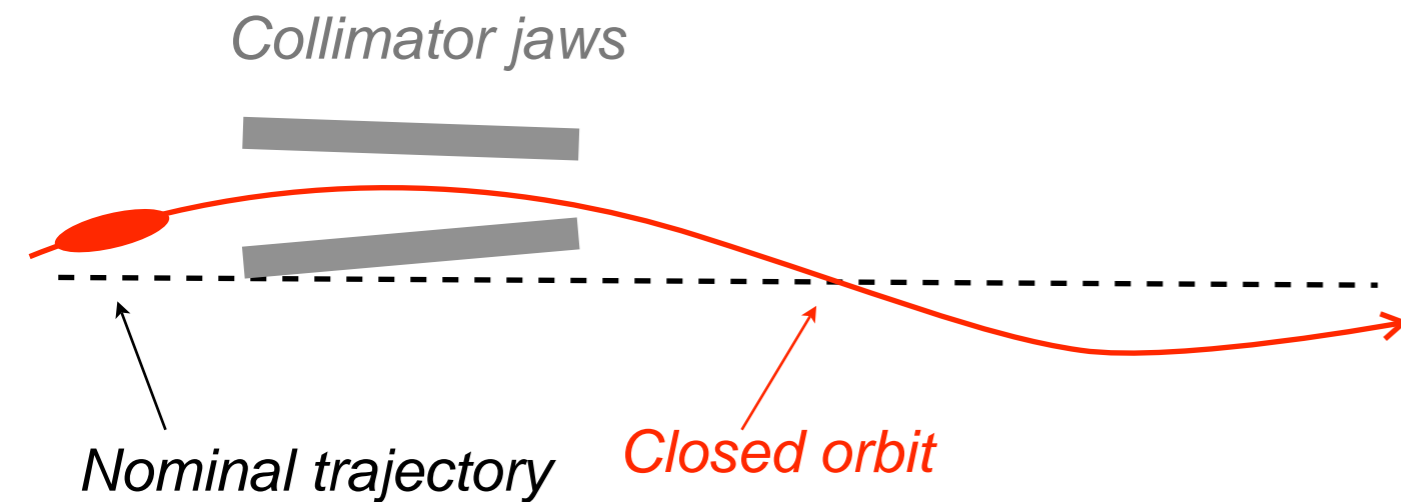


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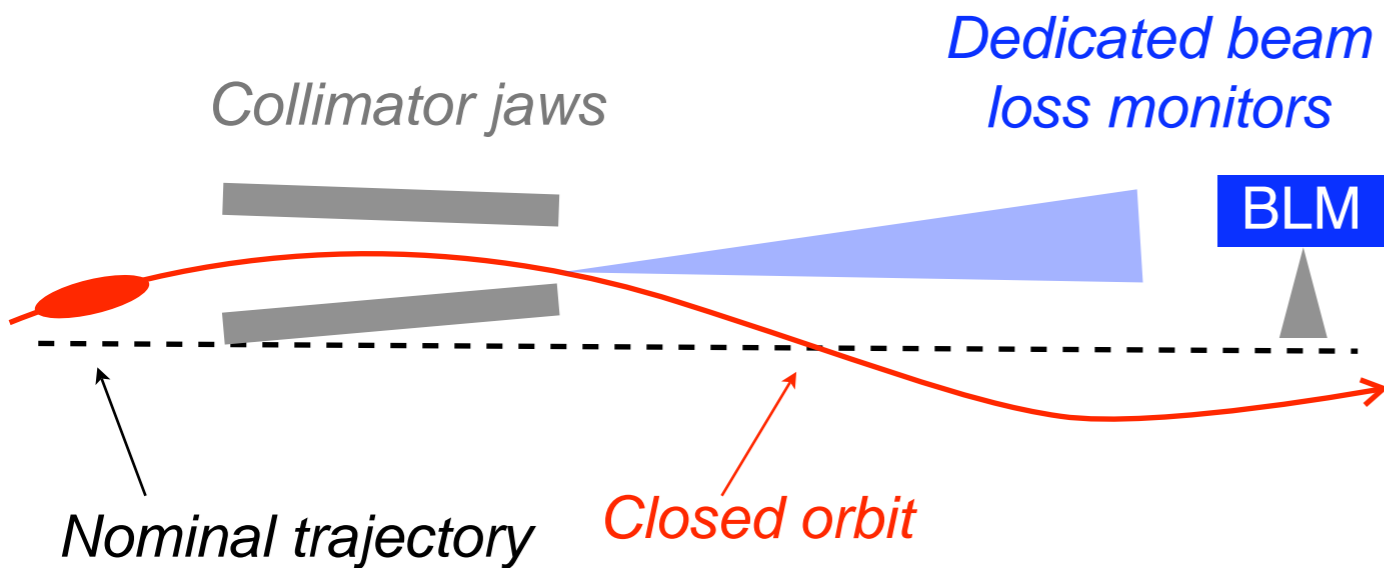
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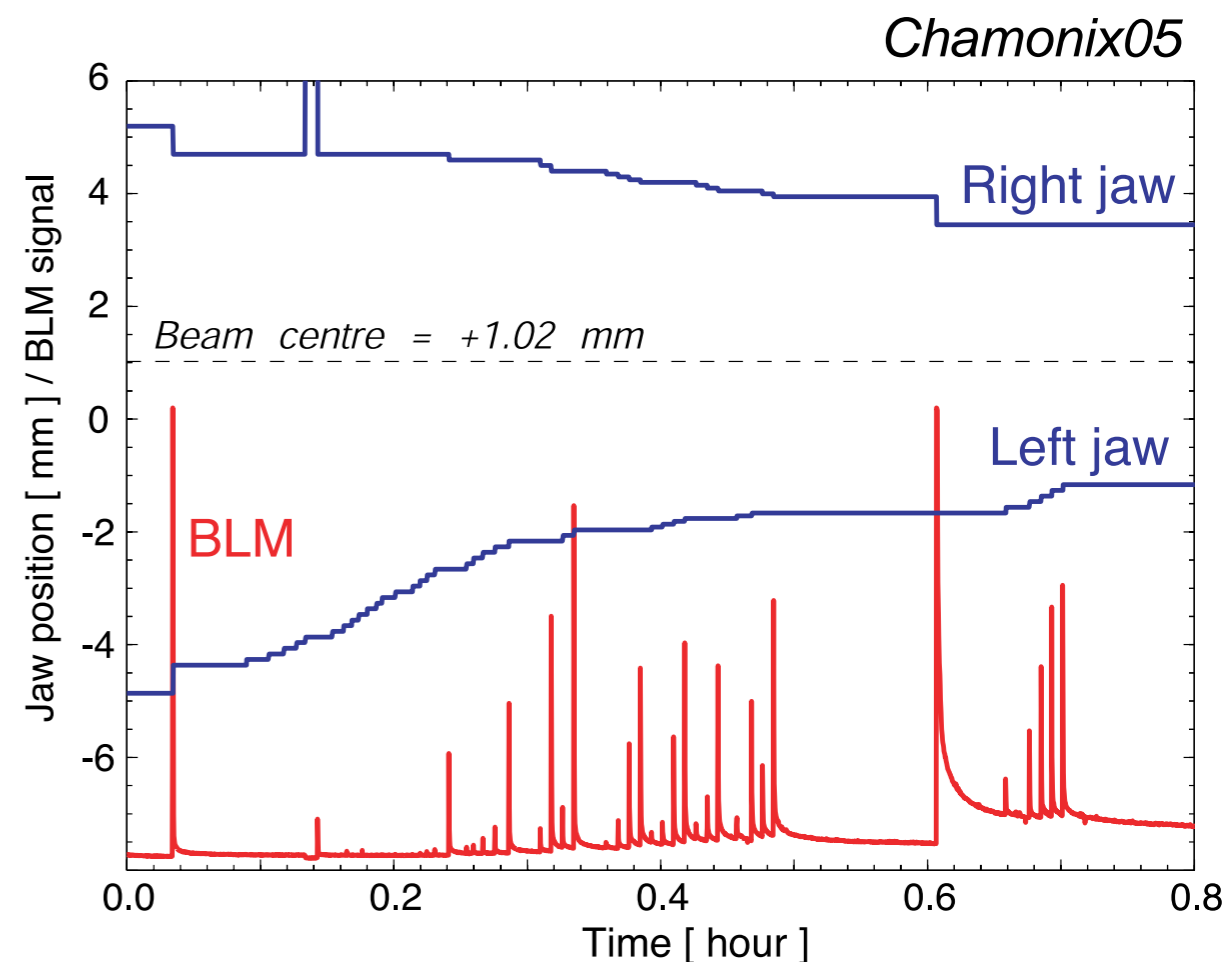
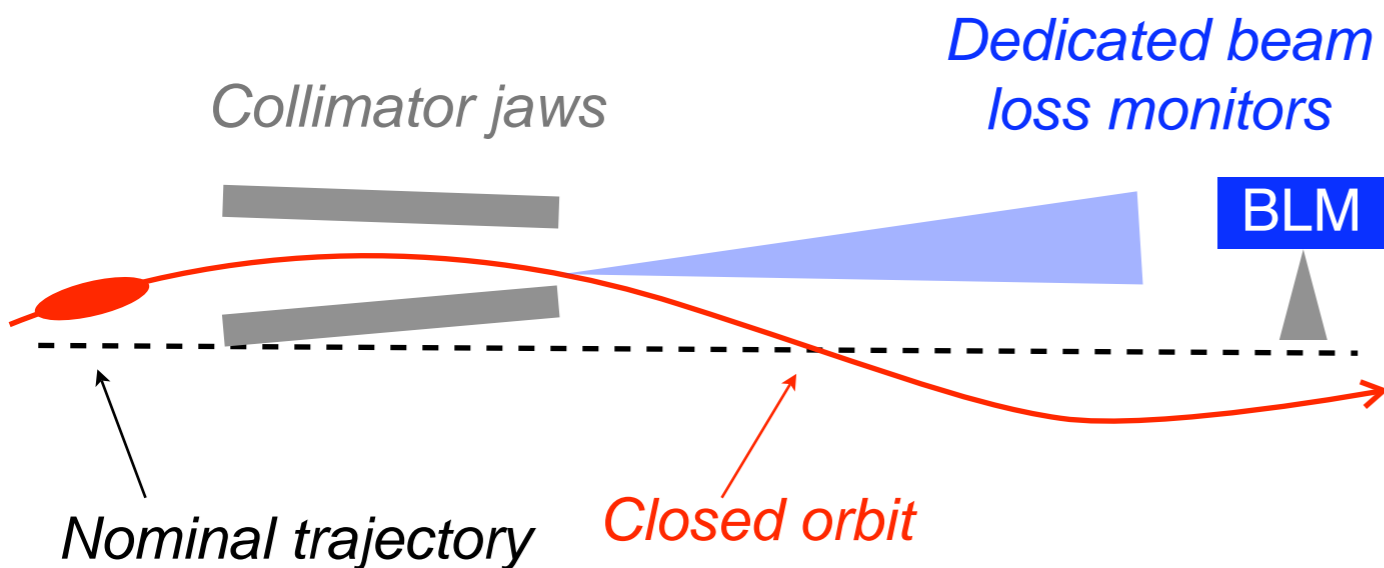
Beam-based alignment with BLMs



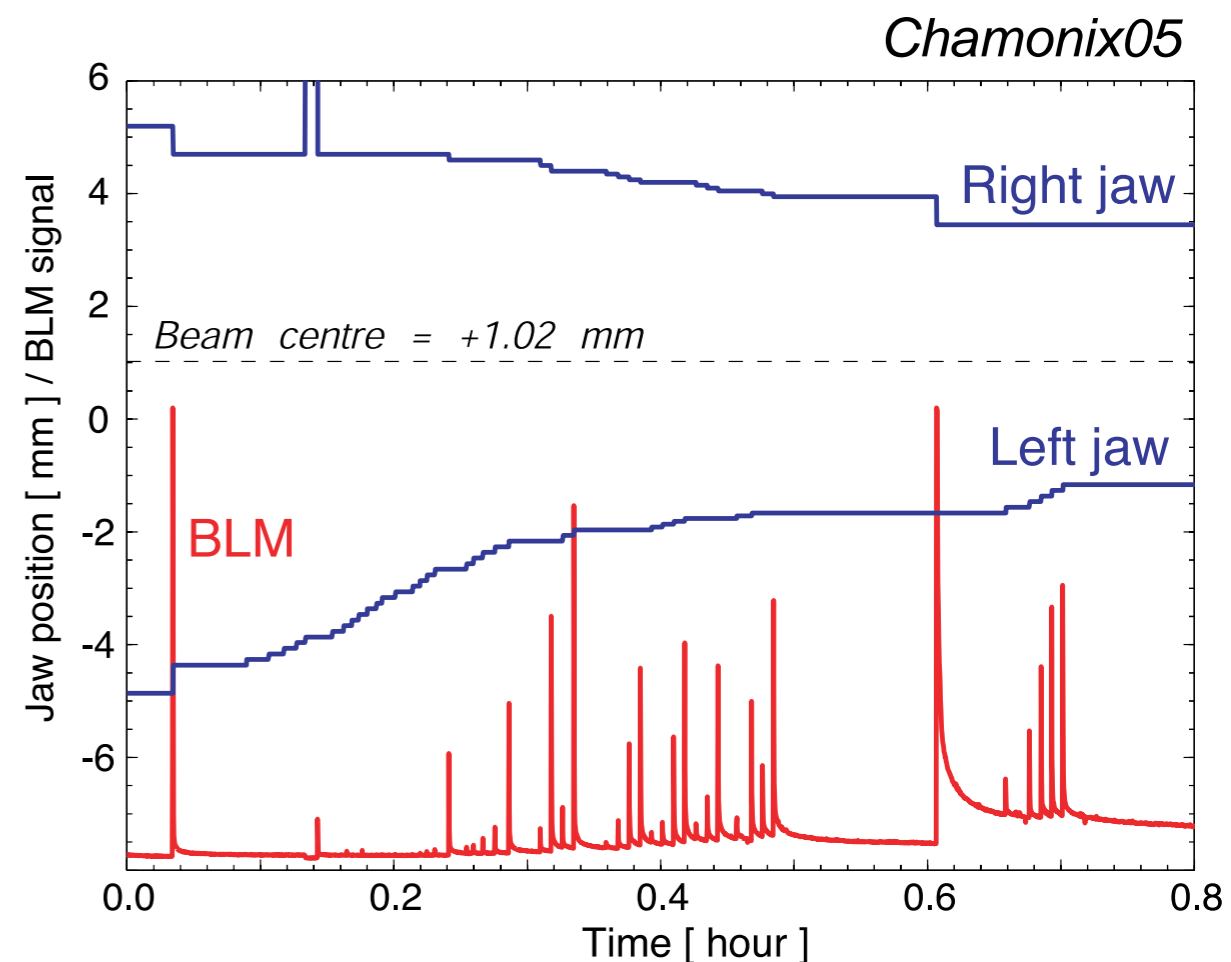
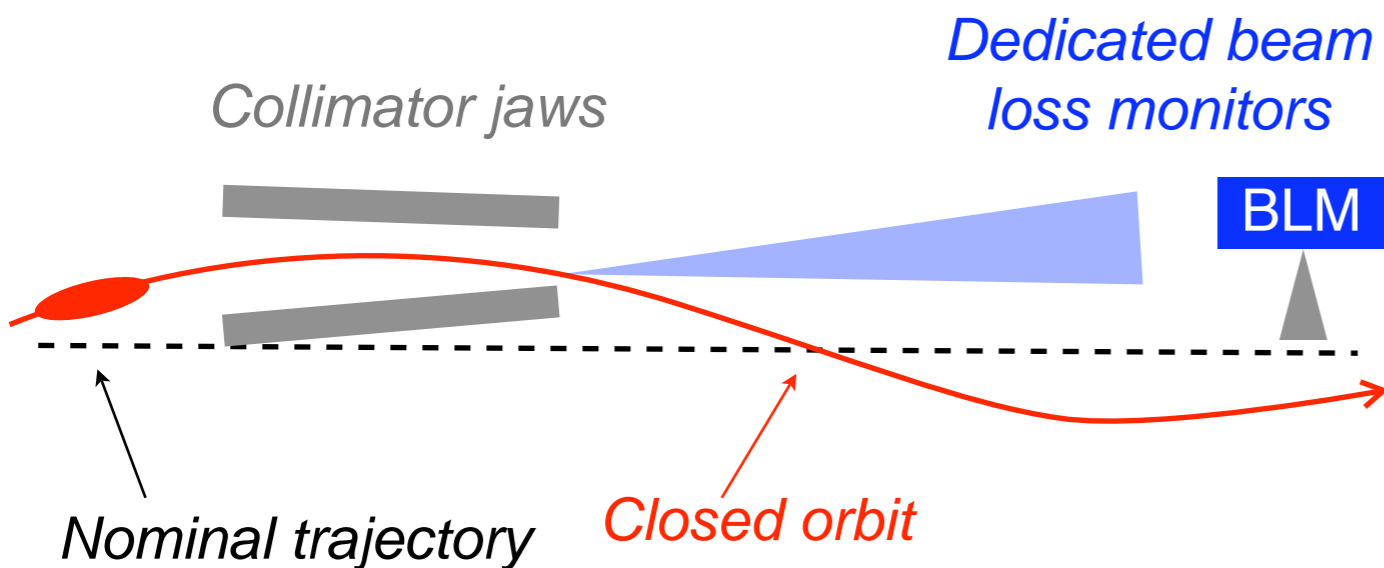
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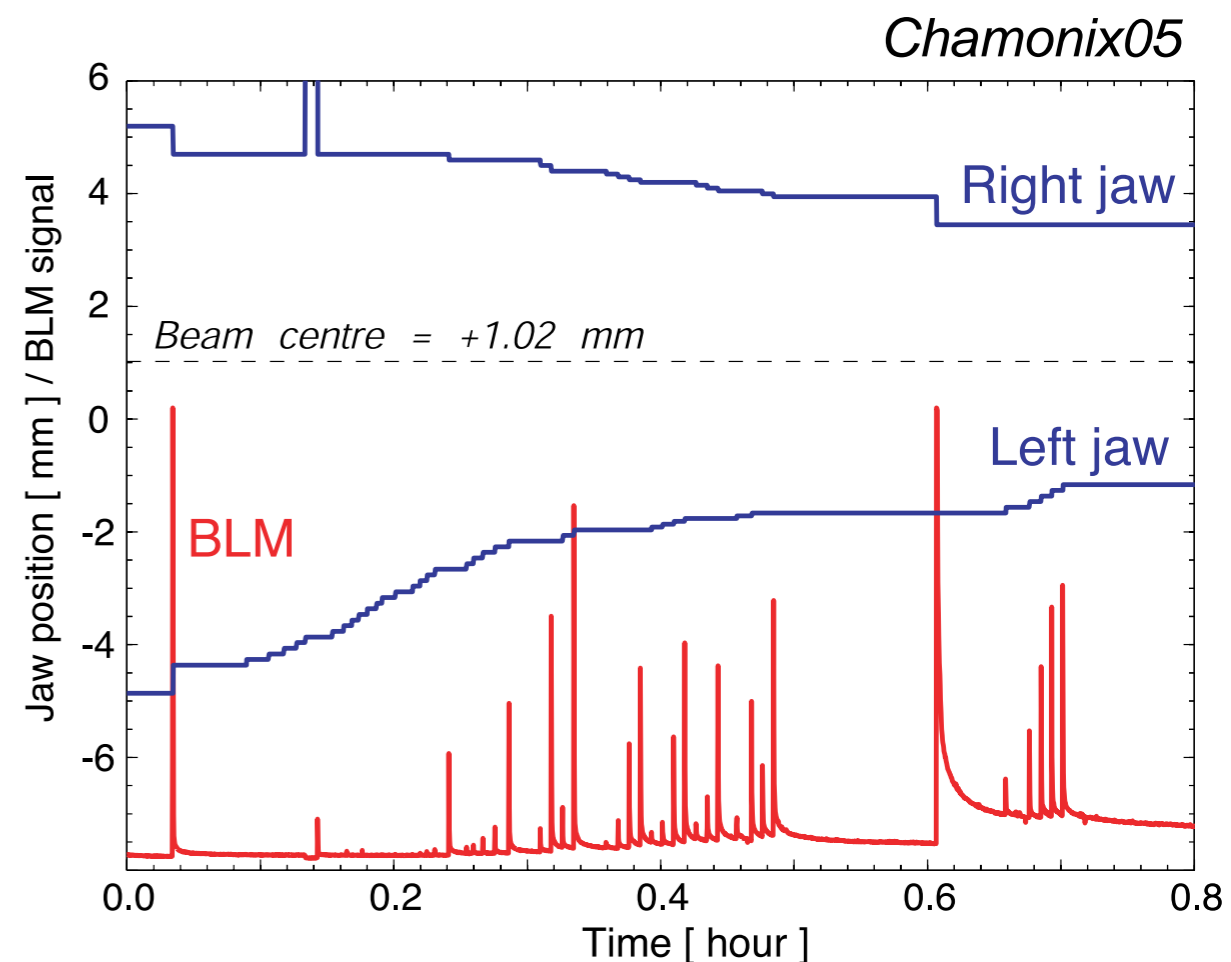
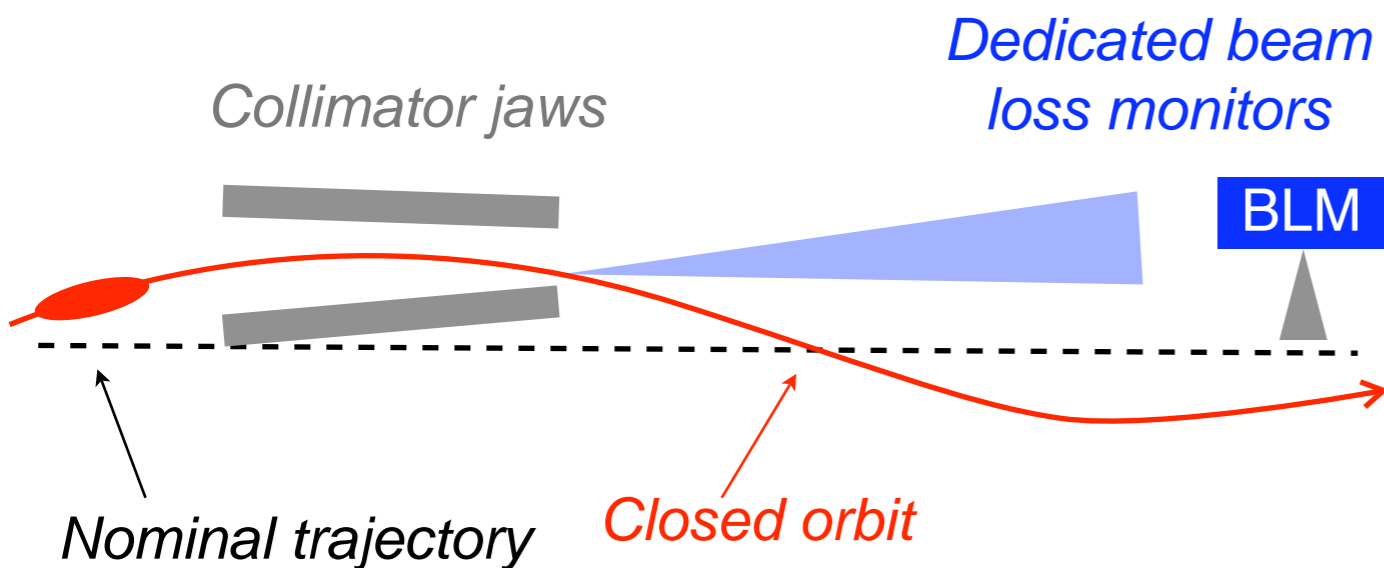


Beam-based alignment with BLMs



Basic requirements for the beam-based alignment:

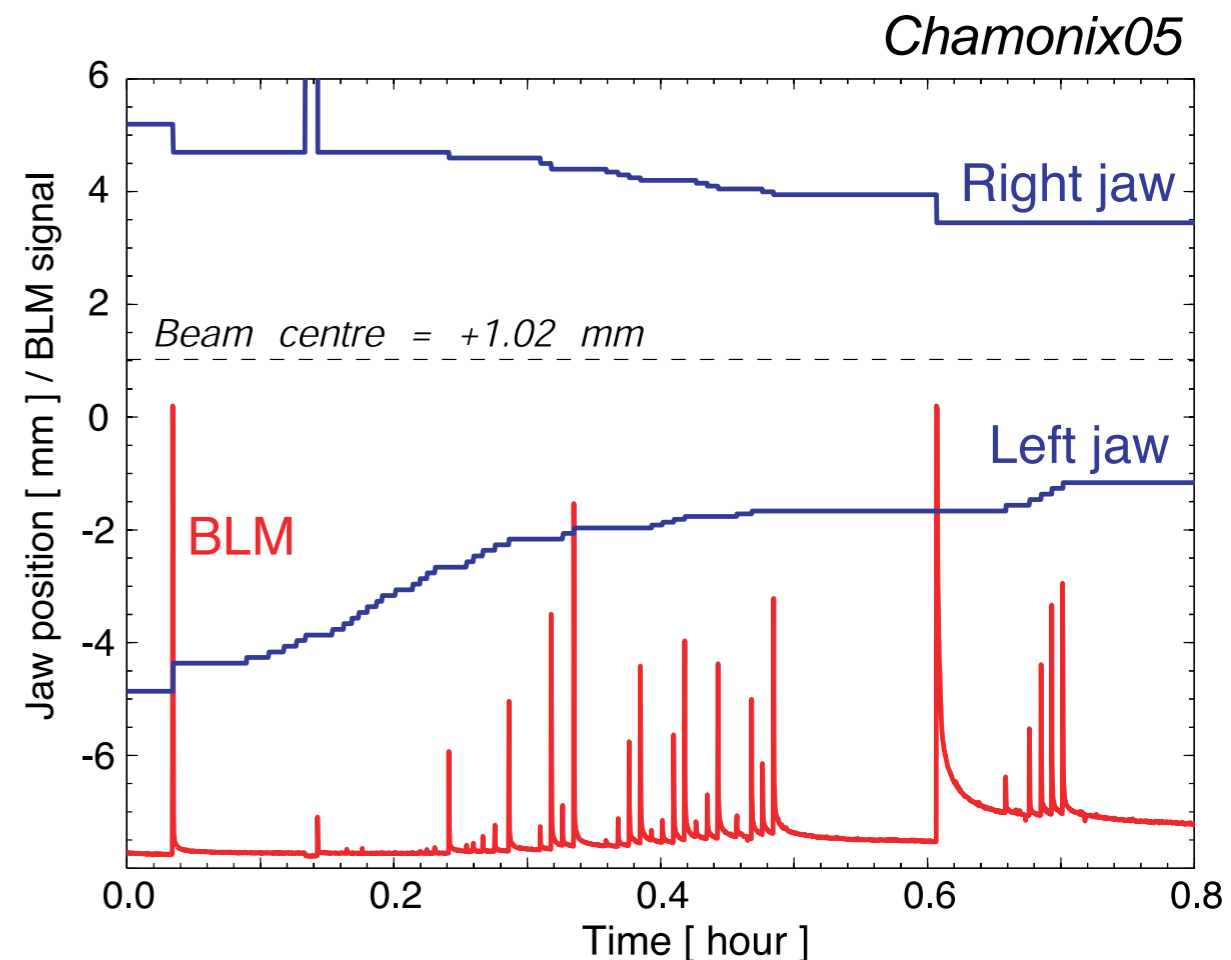
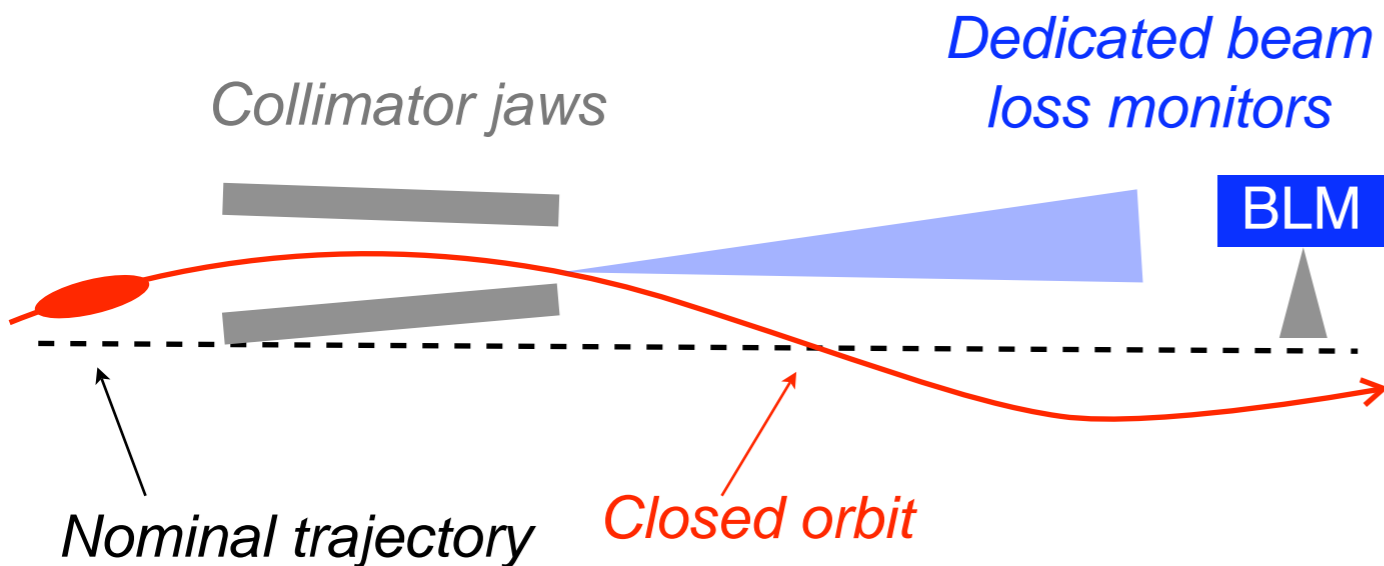
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Basic requirements for the beam-based alignment:

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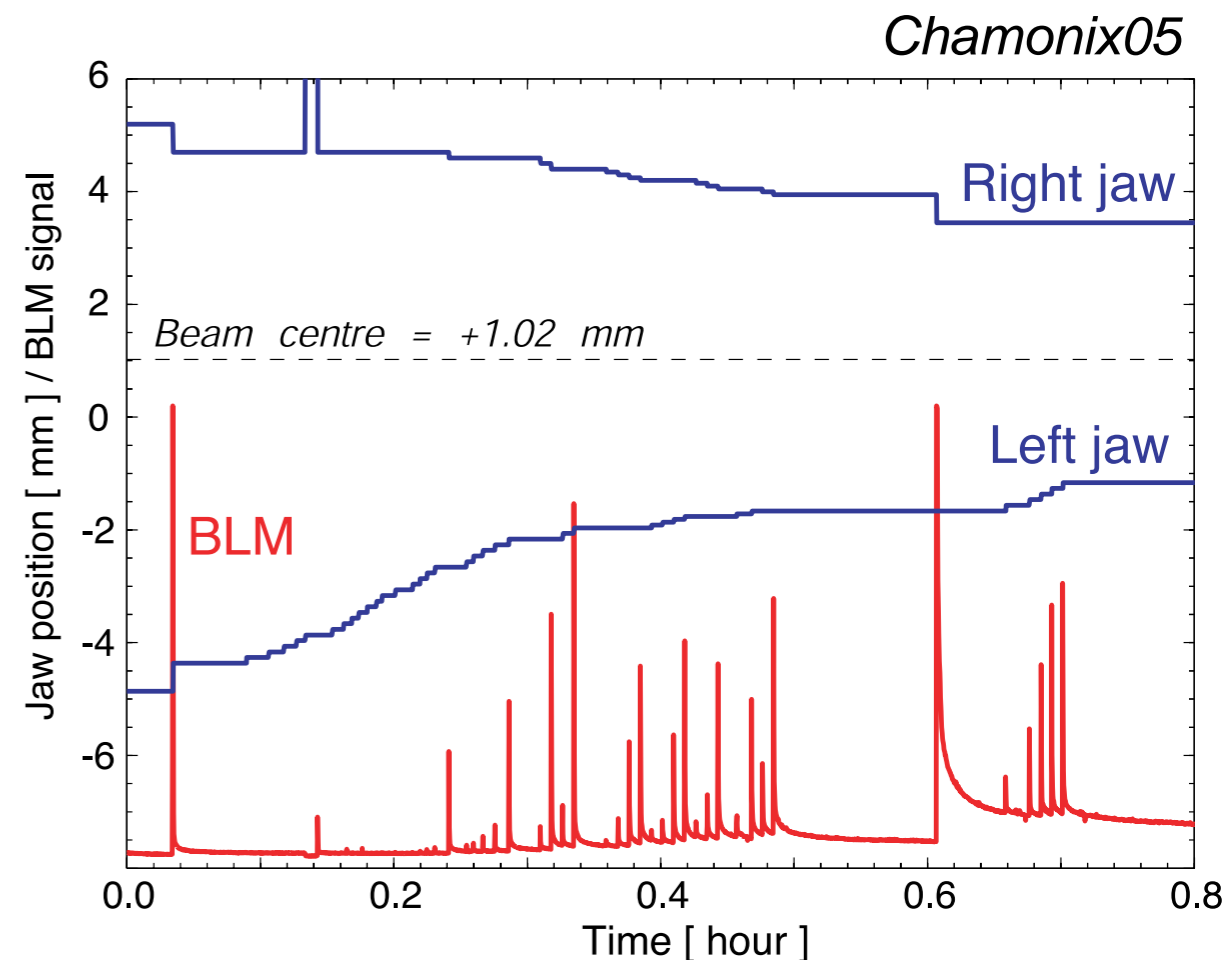
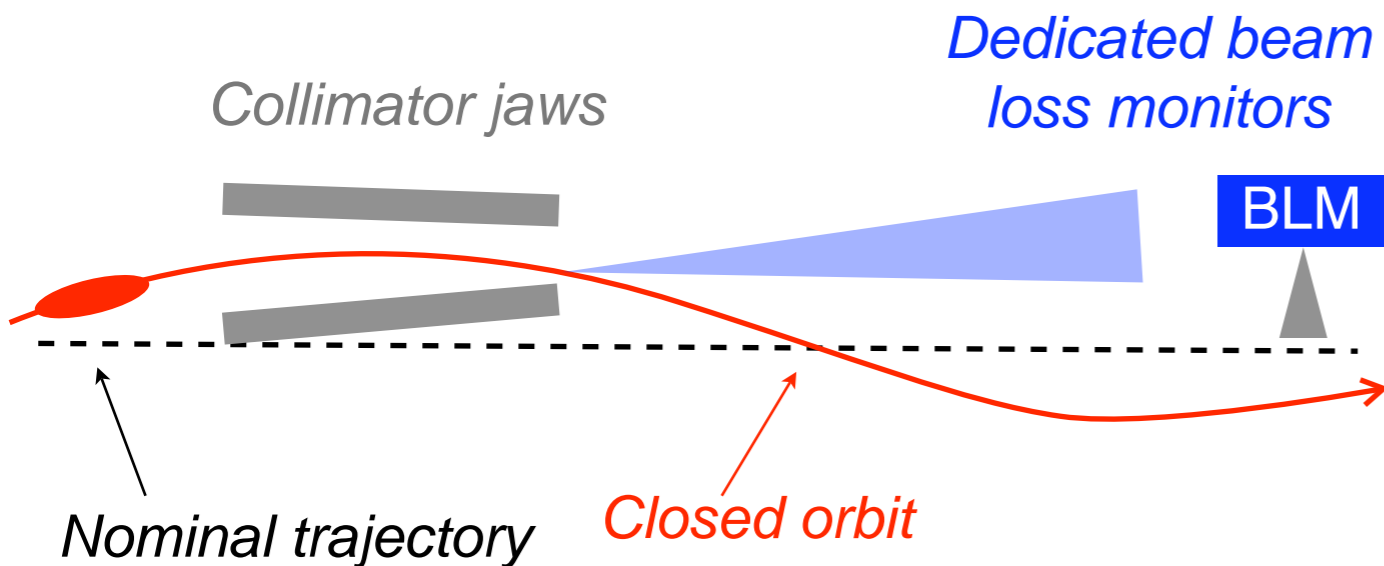
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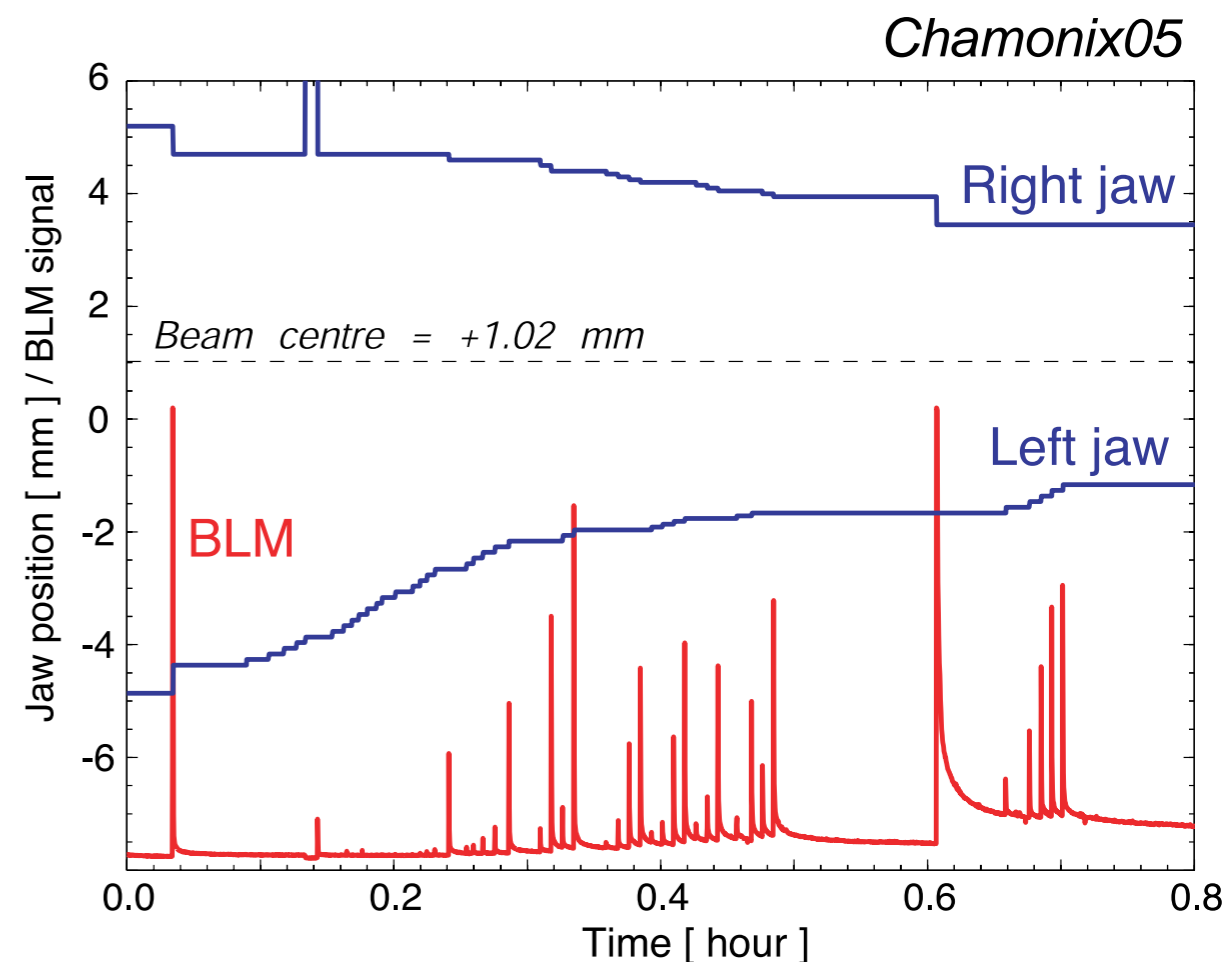
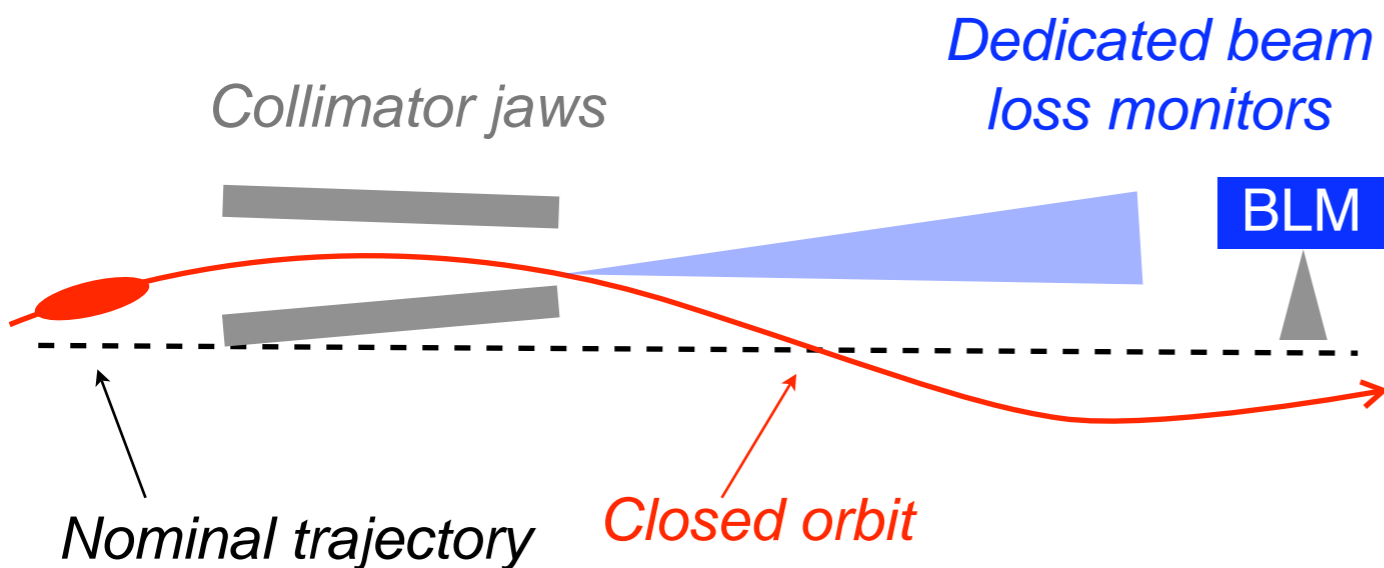
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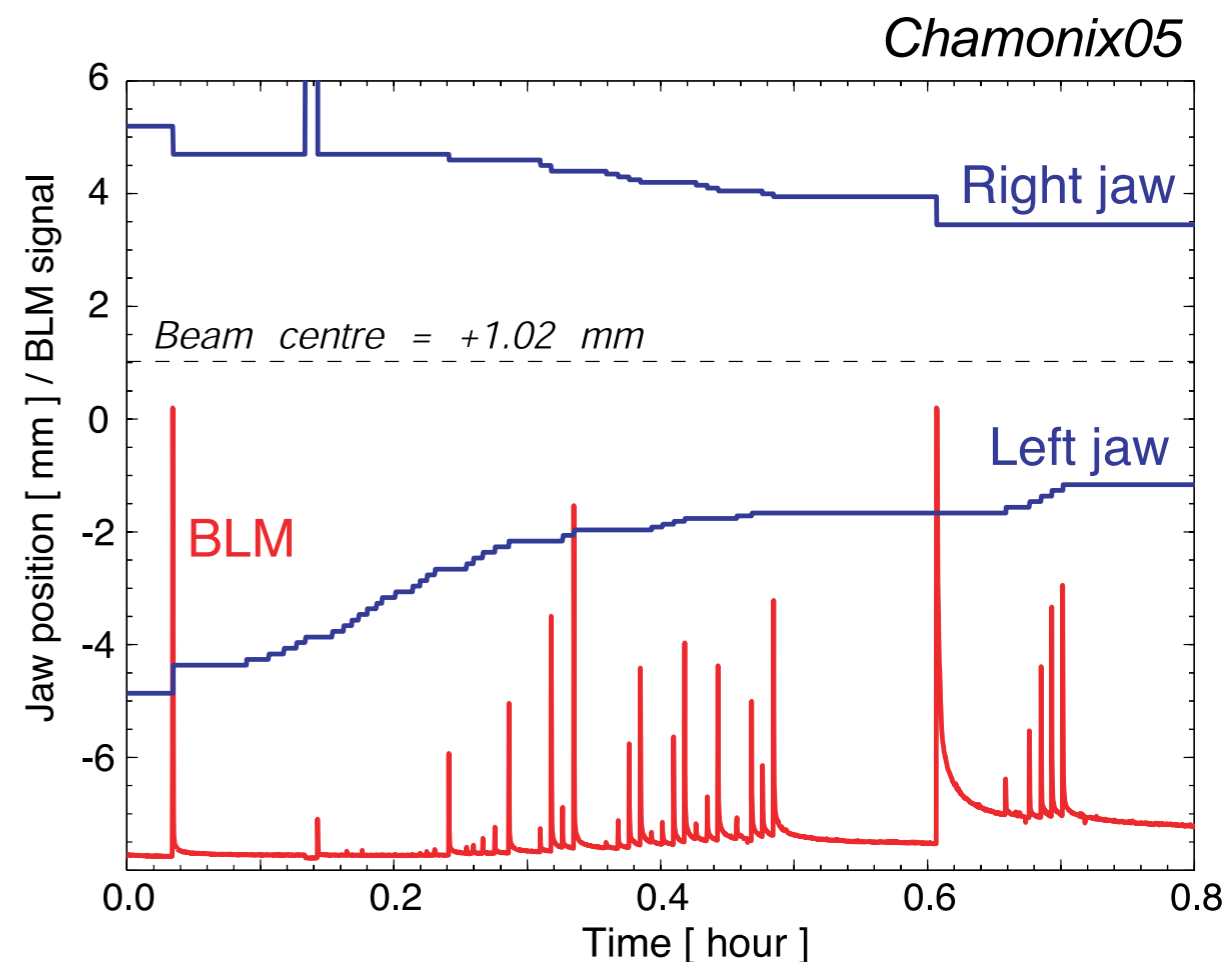
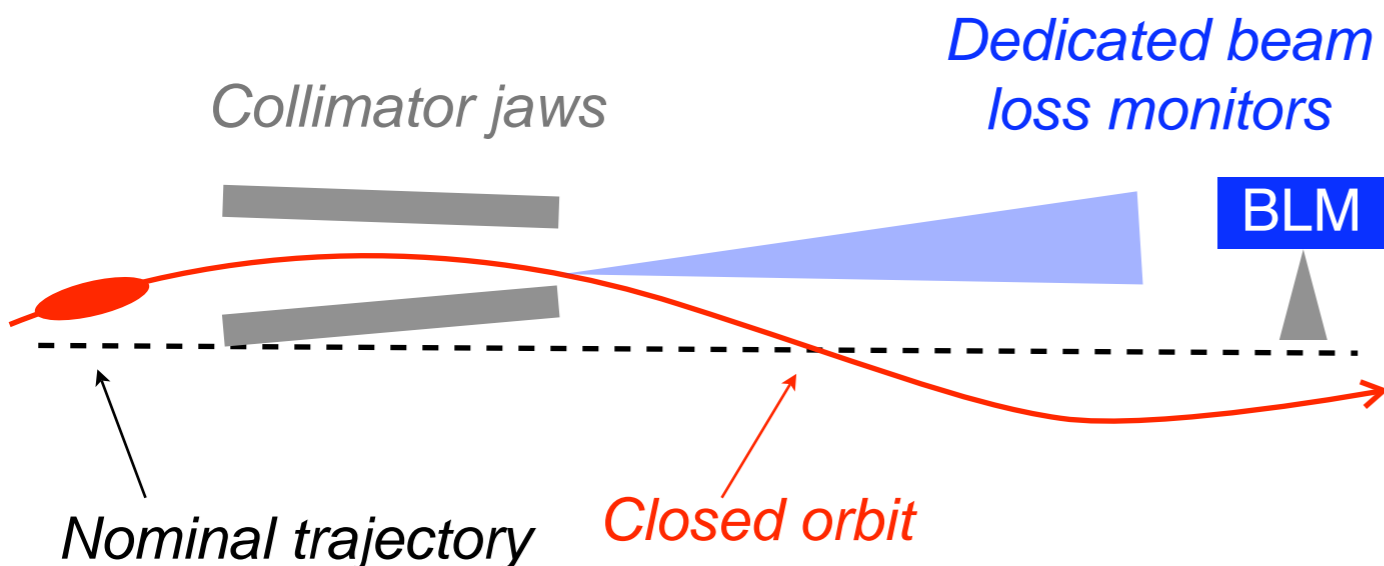
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- Monitor the switch status (10 per collimator)
- Efficient tool to send settings, various coordinate systems (*single corners, average, angles*)
- Flexibility of plotting tools (*lin/log axes, choose BLM, choose left/right jaw, adjust calibrations, ...*)

Setting panel

File Settings Display info

Jaw corners Positions/Angles Increment

Set increments of jaw positions/angles

Left POSIT [mm]:

Right POSIT [mm]:

Left ANGLE [mrad]:

Right ANGLE [mrad]:

Repeat times every sec.

Repeat option finished - 5/5 executed.

Left Jaw UP-IN UP-OUT DW-IN DW-OUT

Right jaw UP-IN UP-OUT DW-IN DW-OUT

Anti COLL UP DOWN

Positions readout from the low-level

Left UP 1.934 Gap UP 5.556

Left DW 2.255 Gap DW 7.177

Right UP -1.778 Centre UP 0.078

Right DW -1.906 Centre DW 0.175

Display jaw: Left Jaw (dashed) Right jaw (solid)

Positions: Settings LVDT's Resolvers Motor steps

BLM: BLM 1 BLM 2 BLM 3 BLM 4 LogY

Views

Beam loss data [08/11/06 05:22:04]

LHC-type BLM at SS5

Jaw positions [08/11/06 05:22:05]

On-line monitoring of jaw positions

Setting panel

File Settings Display info

Jaw corners Positions/Angles Increment

Set increments of jaw positions/angles

Left POSIT [mm]:	<input type="text" value="-0.025"/>	<input type="button" value="Apply I"/>
Right POSIT [mm]:	<input type="text" value="0.0"/>	<input type="button" value="Cancel last"/>
Left ANGLE [mrad]:	<input type="text" value="0.0"/>	<input type="button" value="Stop all!"/>
Right ANGLE [mrad]:	<input type="text" value="0.0"/>	<input type="button" value="Jaw pos/ang ▼"/>

Repeat times every sec.

Repeat option finished - 8/8 executed.

Different **coordinate systems** (single corners, average + angle)
All conversions done at top-level -> coherent set of settings to the lower levels (fully compatible with LSA TRIM!)

“Repeat” functionality, **“cancel last”**, **“stop”**: efficient control during routine operation

Detailed sensor readout panel

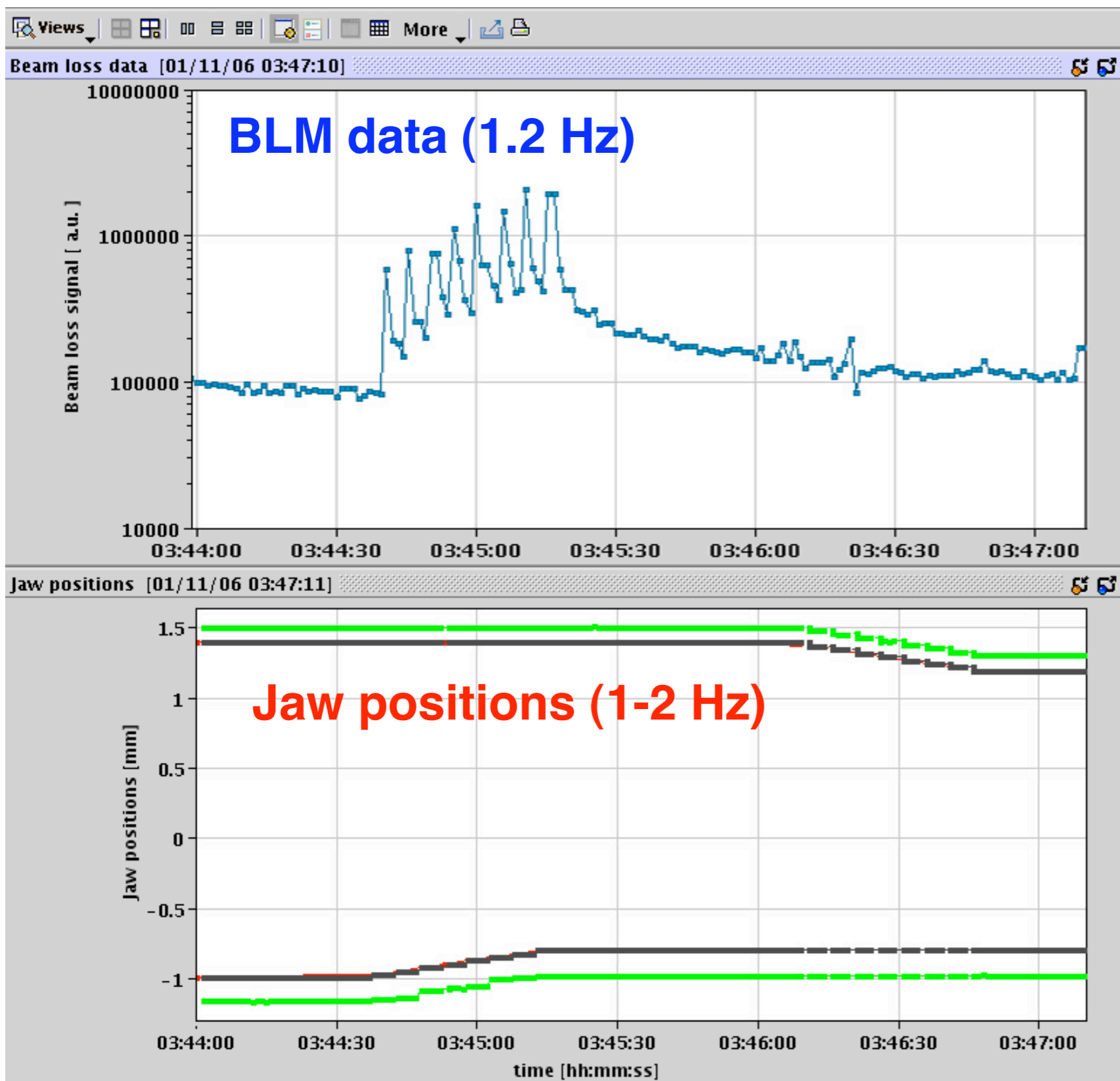
Left Jaw	UP-IN	UP-OUT	DW-IN	DW-OUT
Right jaw	UP-IN	UP-OUT	DW-IN	DW-OUT
Anti COLL	UP	DOWN		

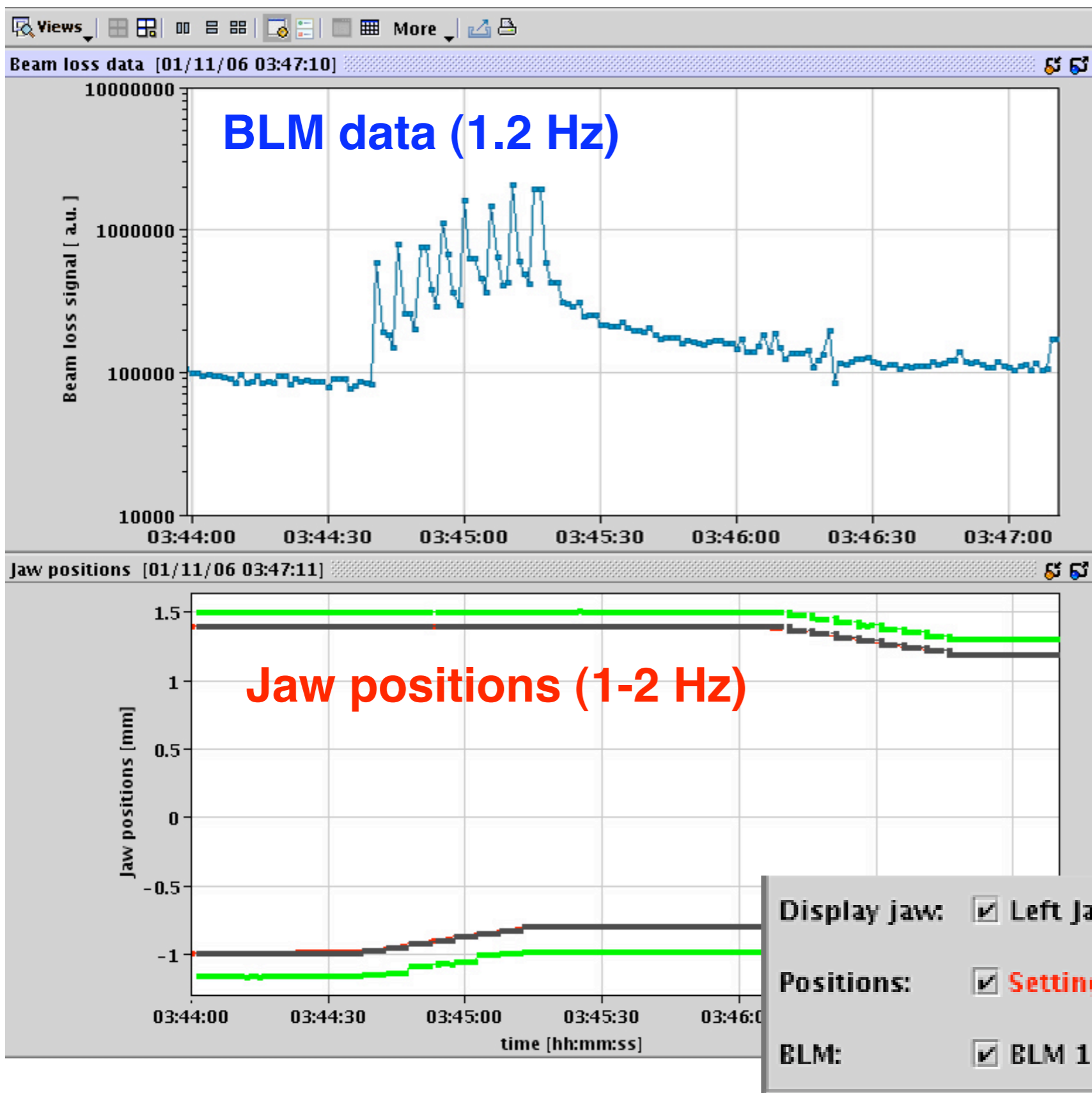
Positions readout from the low-level

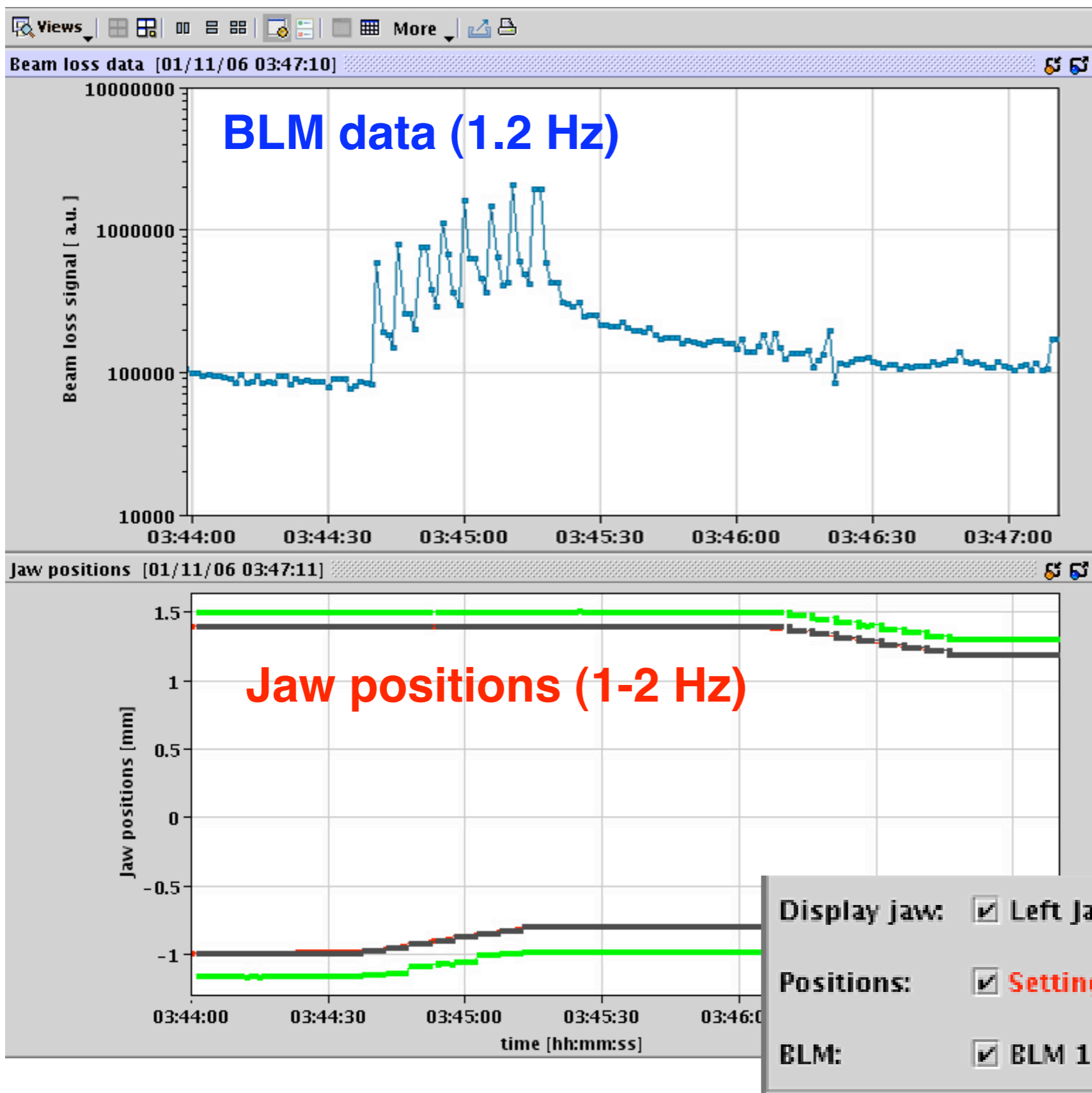
Resolvers ▼	Left UP	30.824	Gap UP	27.742
Jaw edges ▼	Left DW	30.374	Gap DW	27.779
	Right UP	3.082	Centre UP	16.953
	Right DW	2.595	Centre DW	16.484

Status of **10 switches** always monitors (1 Hz)

Readout of **14 sensors** (4 motors, 4 resolvers, 6 LVDT s) and **calculation** of gaps, average positions, angles, ... for all of them







This is like in the off-line analysis!

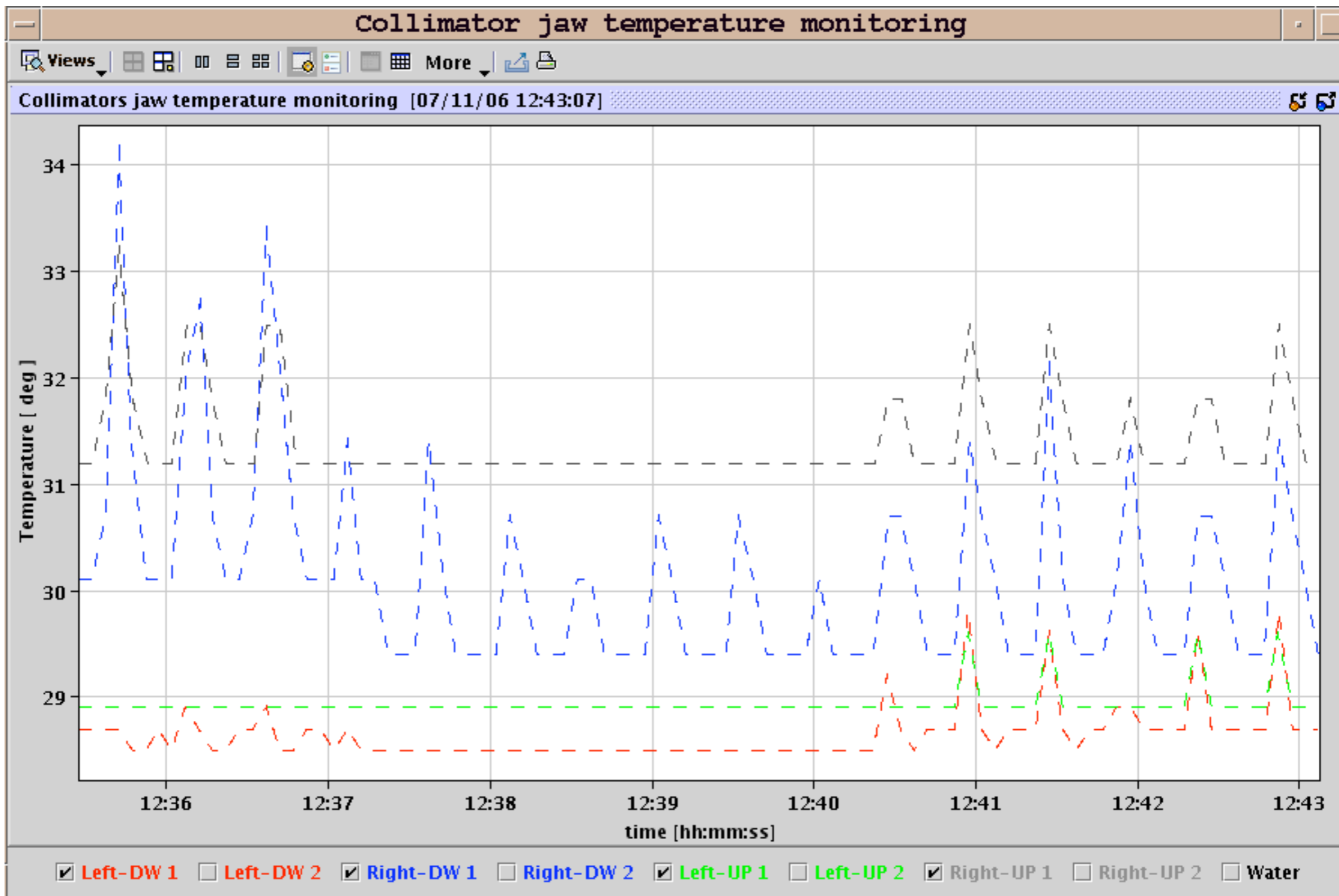
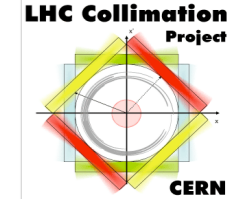
Overall impression:
 Single collimator control worked **very well**, the proposed application fully fulfills the LHC requirements

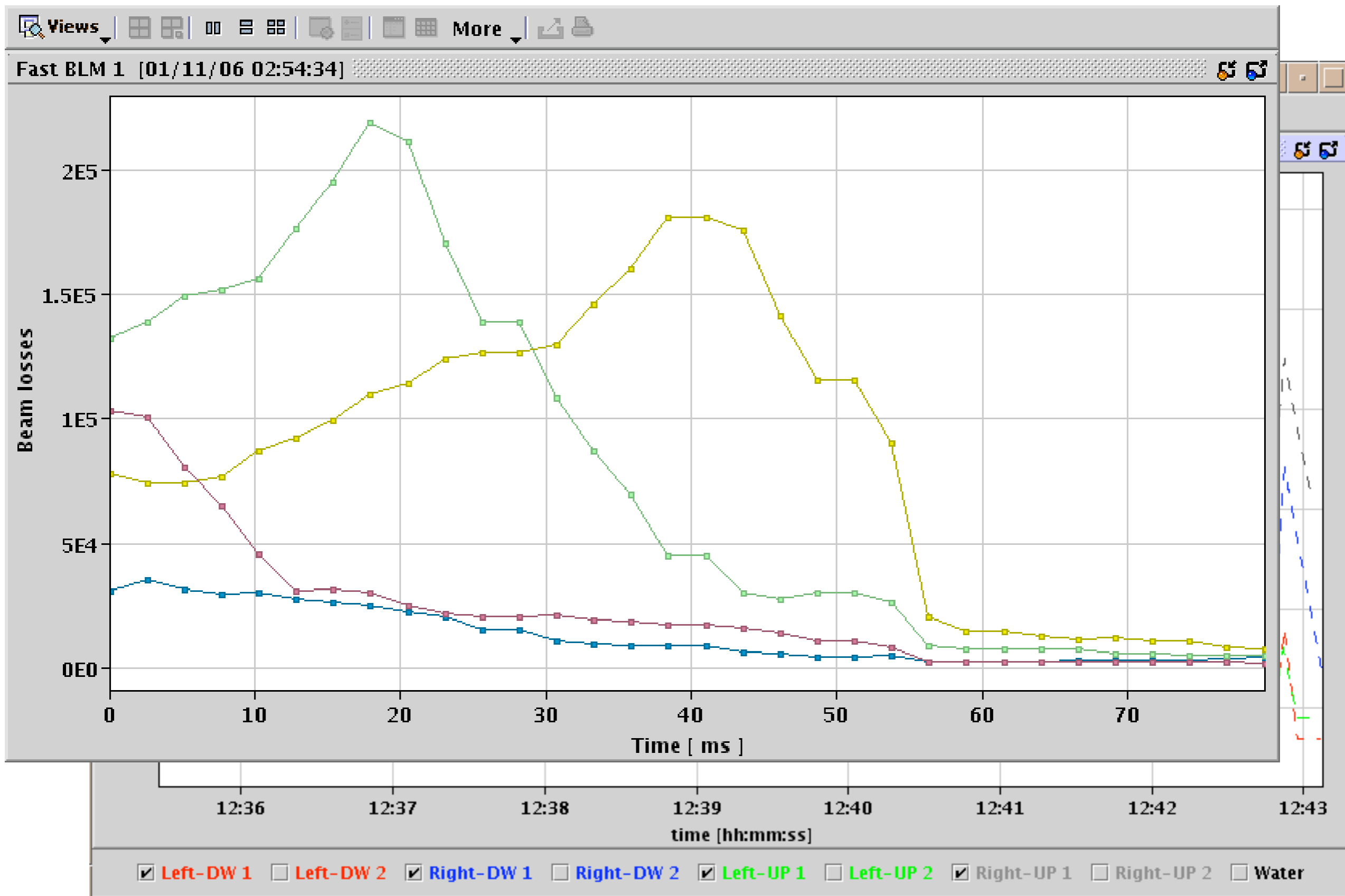
Missing: Conversion to beam units (requires on-line optics model)

Plot setting panel



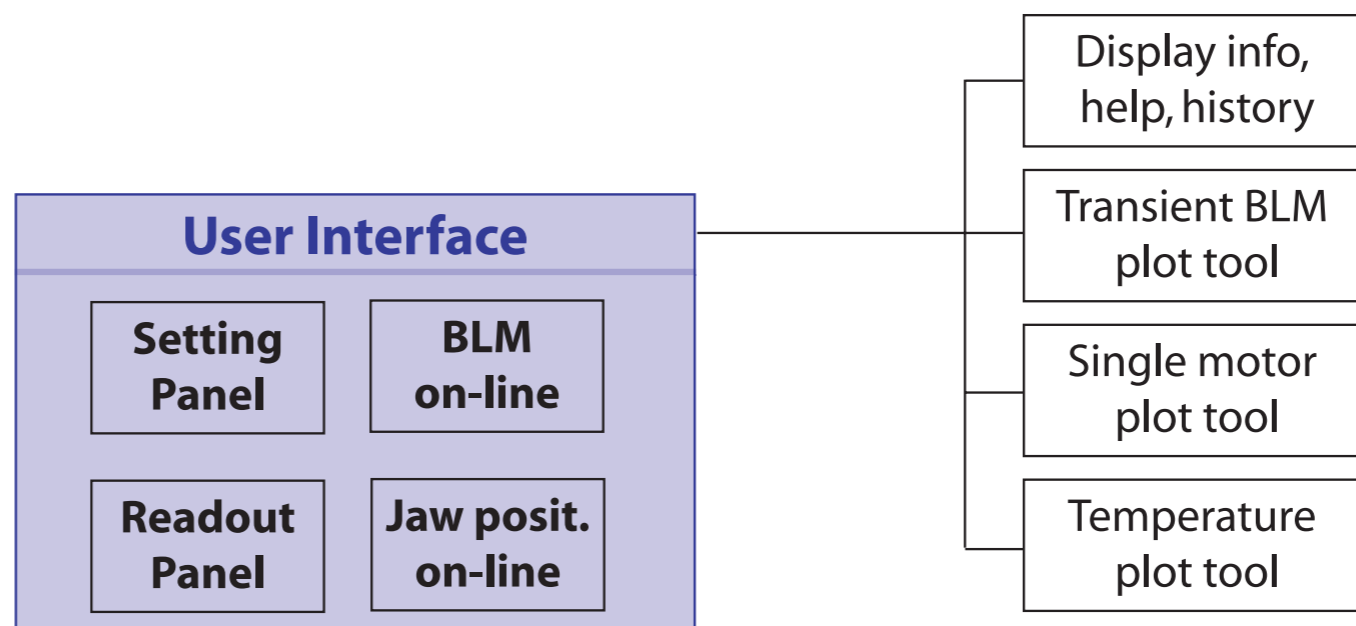
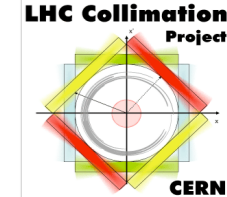
Additional features





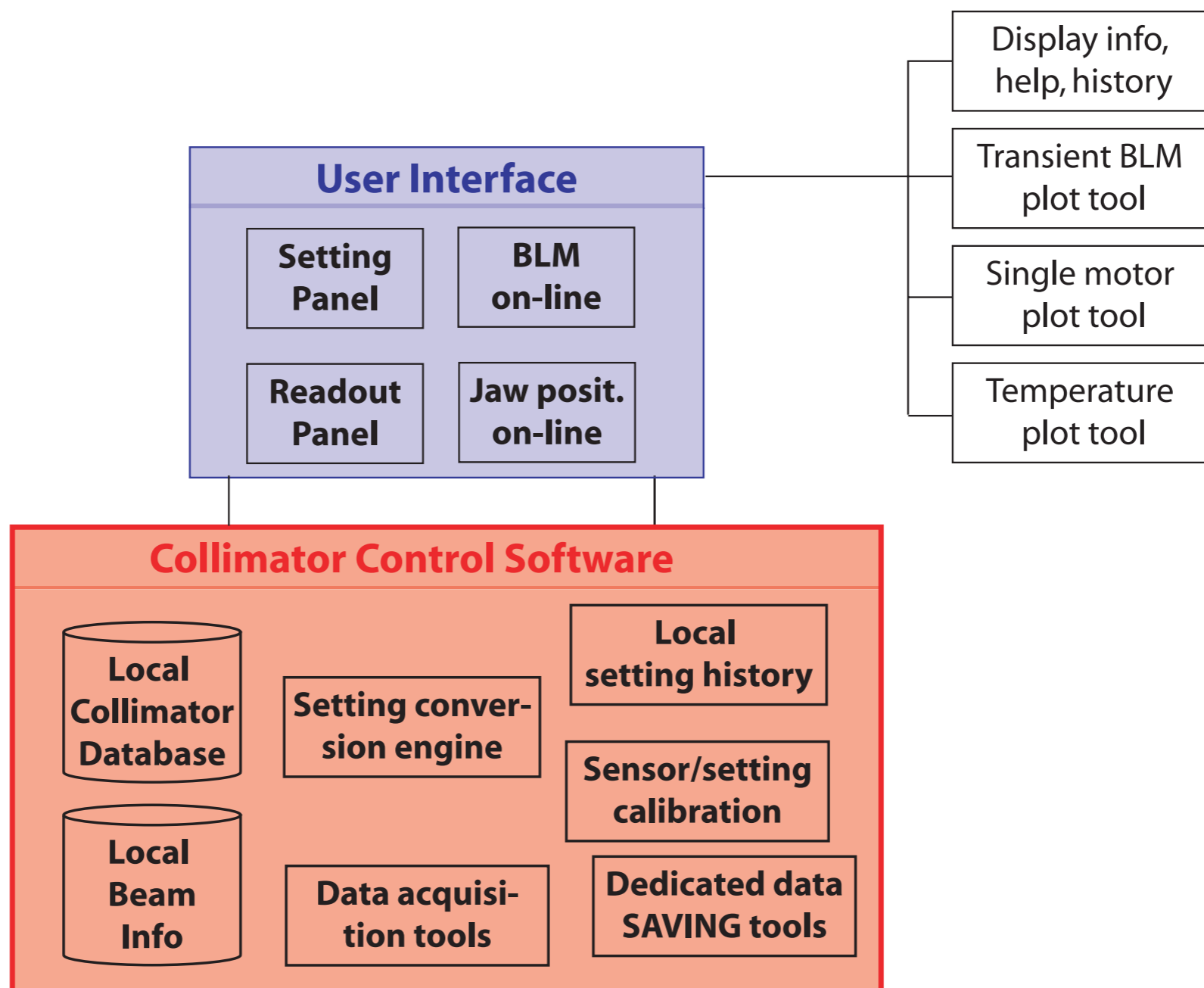
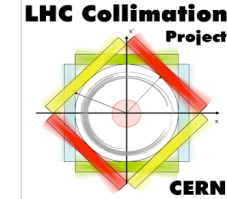


LHC application: SPS implementation



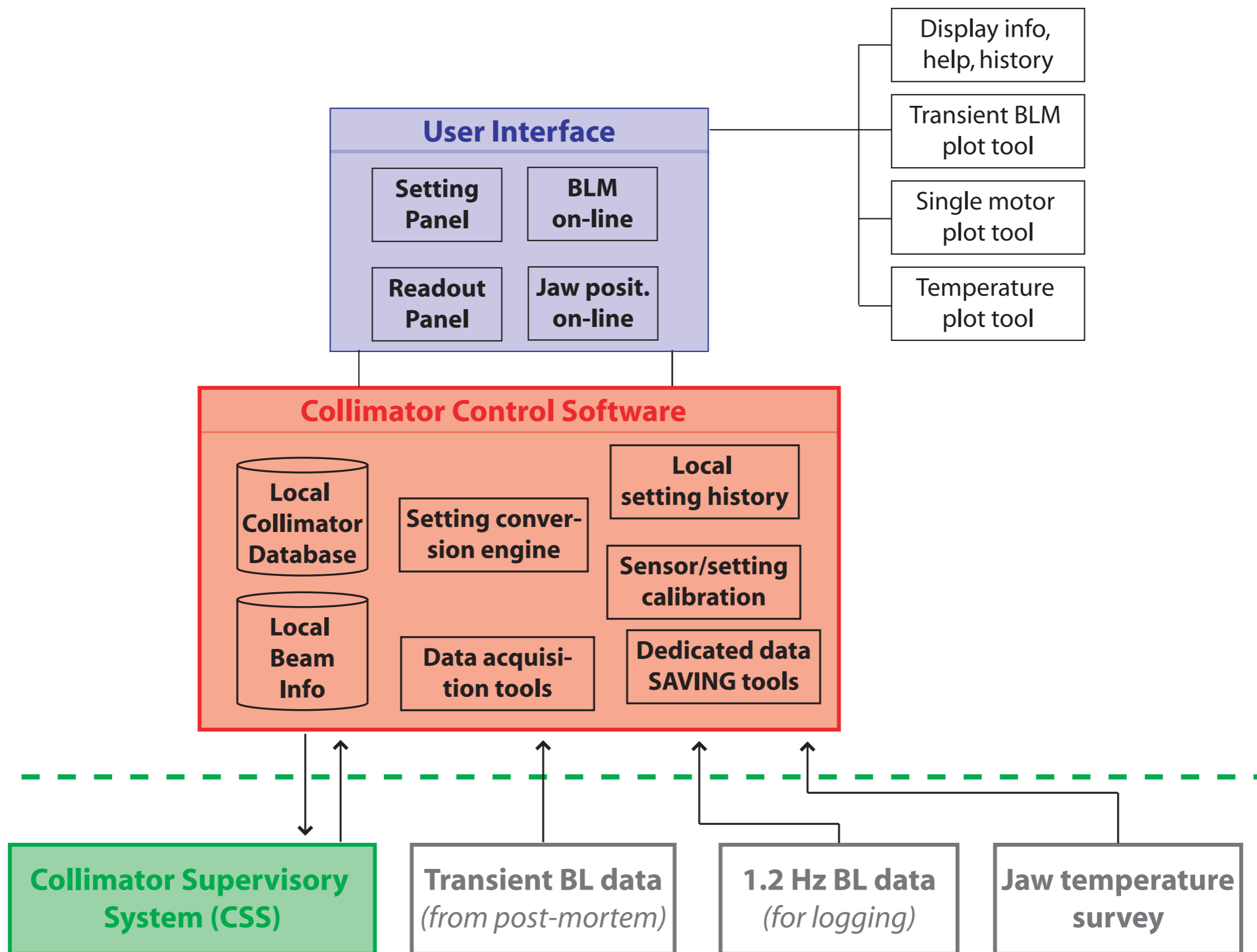
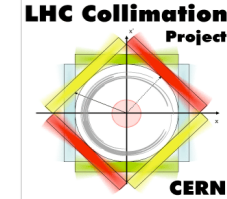


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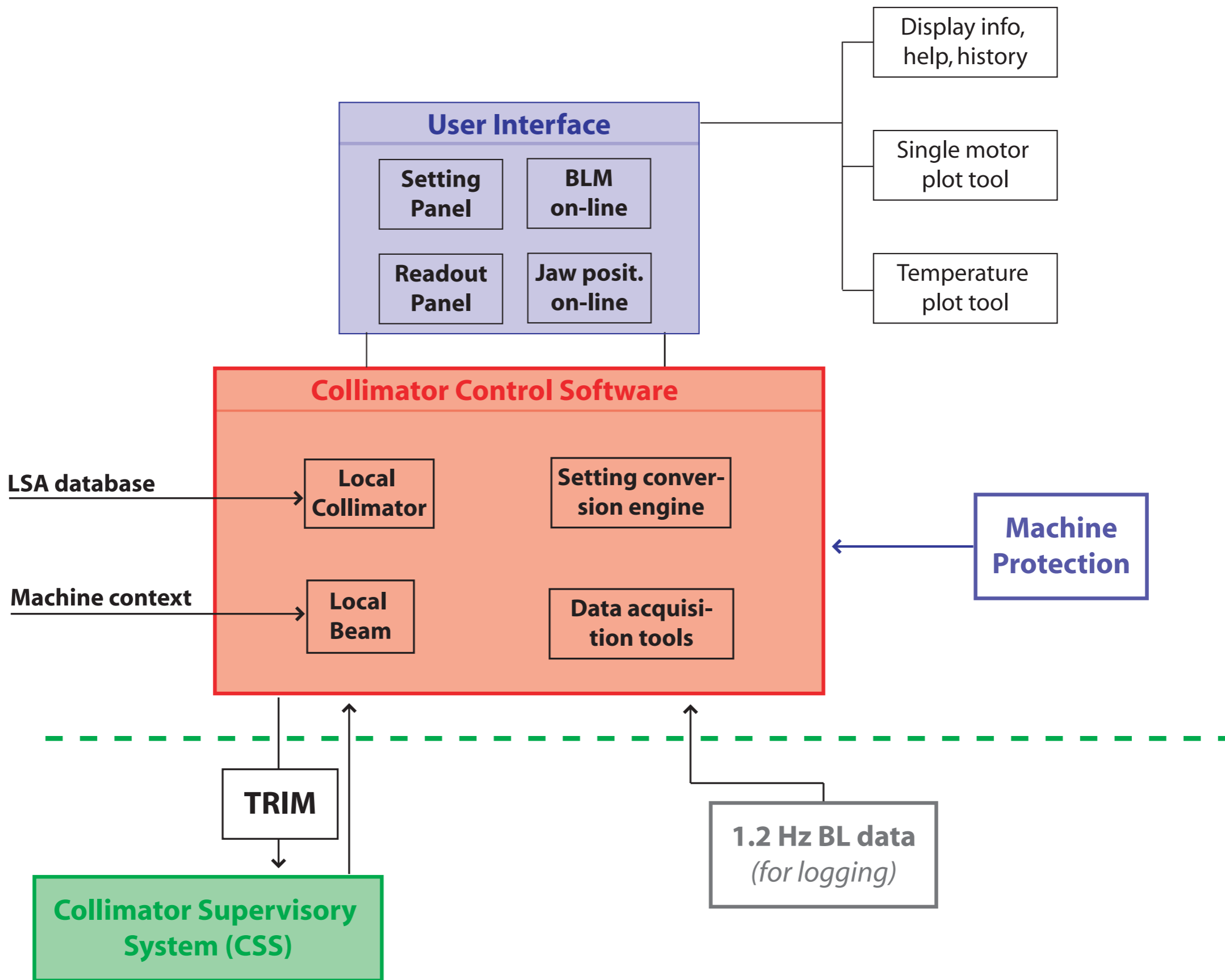




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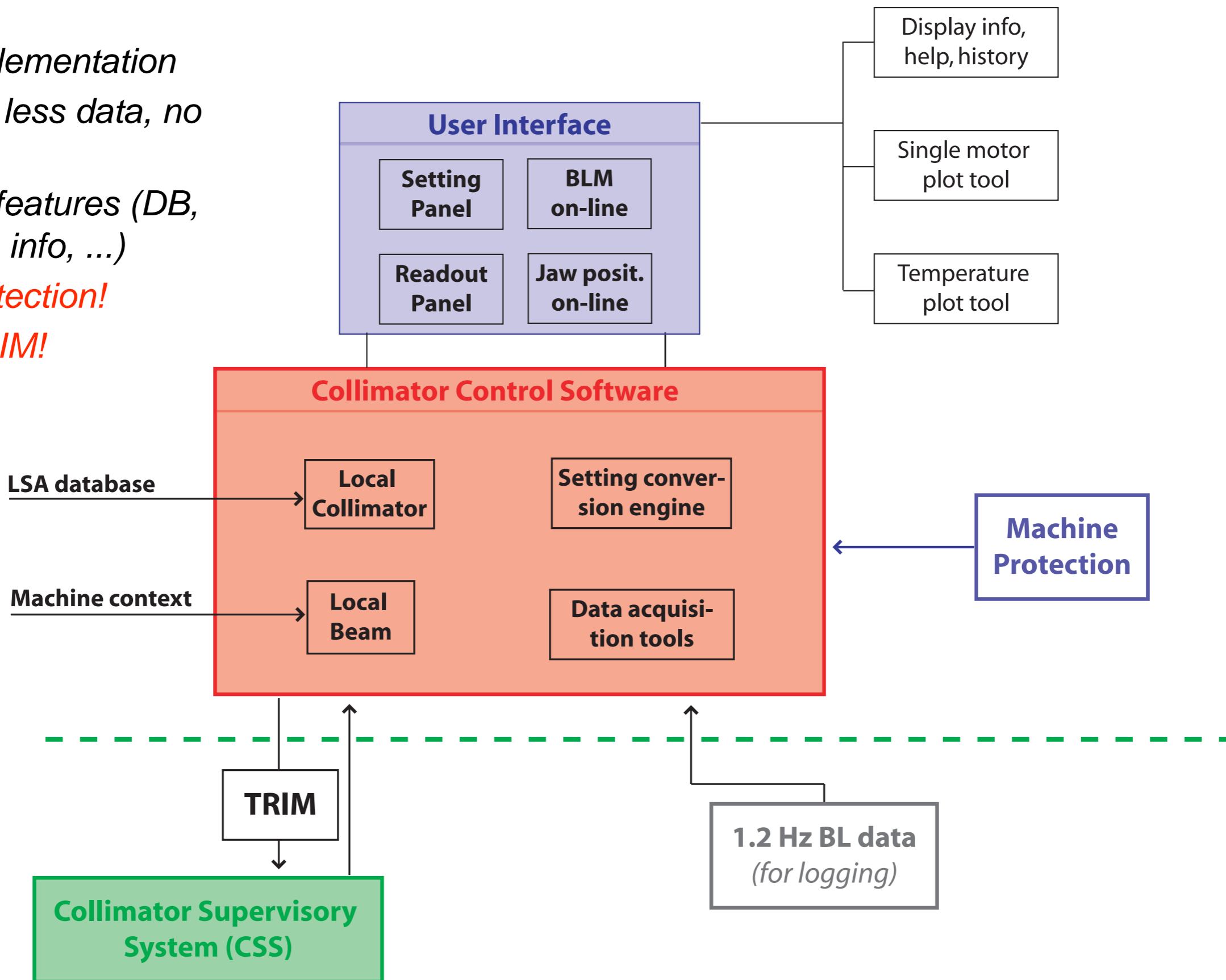


Final LHC implementation



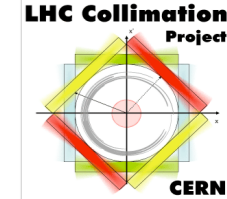
Final LHC implementation

Slimmer implementation
Collection of less data, no data saving
Rely of LSA features (DB, optics, beam info, ...)
Machine protection!
Use LSA-TRIM!

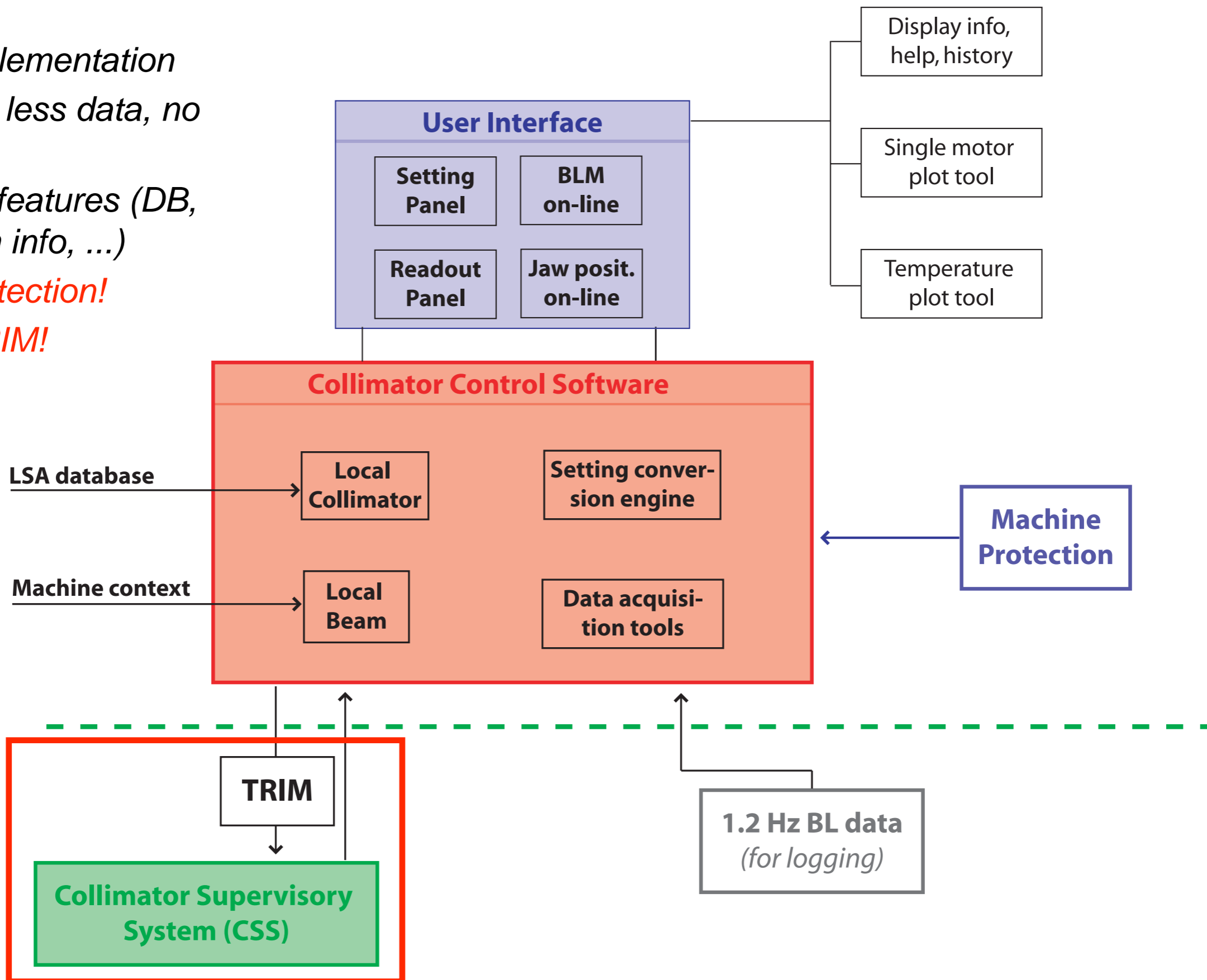




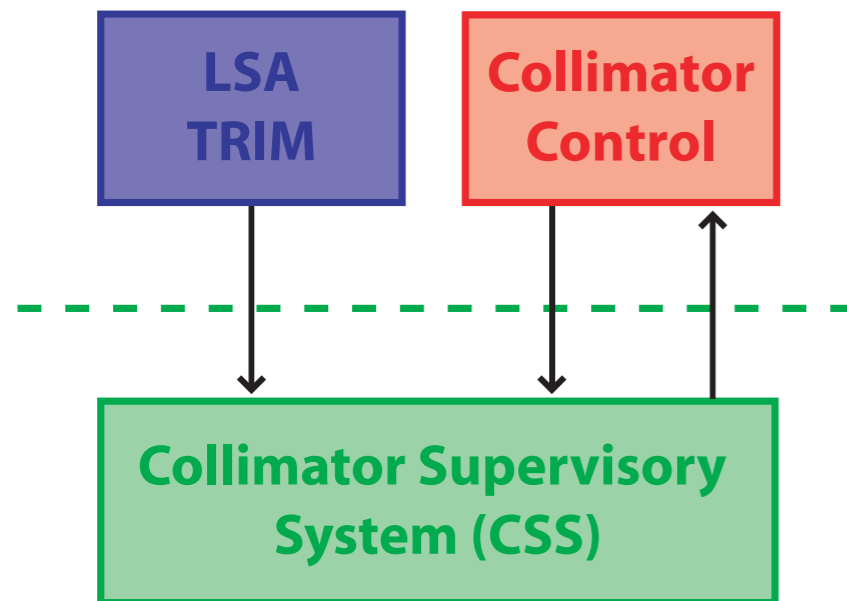
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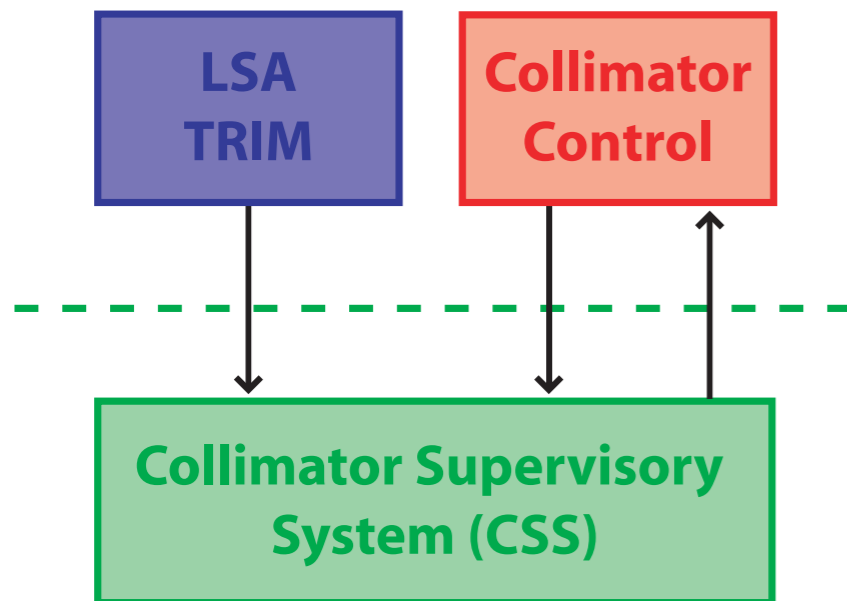


SPS test 2006

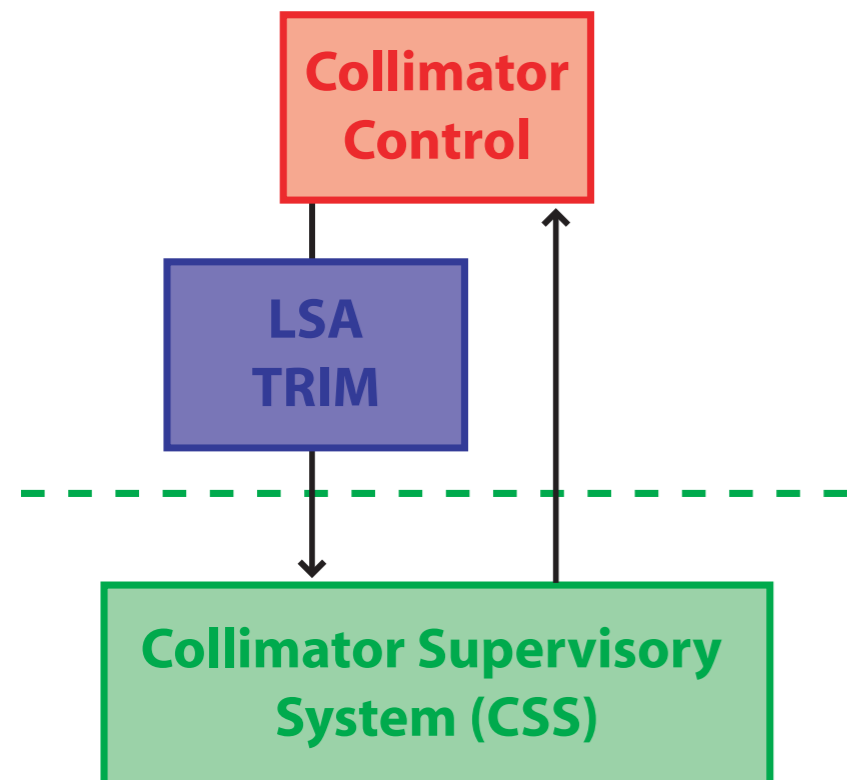


- **LSA TRIM** → control FESA devices at the LHC
- It provides: traceability (setting history), function generation/editing within machine contexts, *MCS functionalities!*
- *Collimator control through TRIM was setup !*
- All the required infrastructure links were setup: LSA database, setting generation, drive-hardware, ...
Thanks: D. Jacquet, M. Lamont, L. Normann. J. Wenninger
- Next step: call TRIM within our application (*TRIM-CLIENT*). *Basically there but not yet tested*
- Our setting philosophy compatible with TRIM: 4 absolute positions, conversions done at top level
- Potential issue of response time?

SPS test 2006



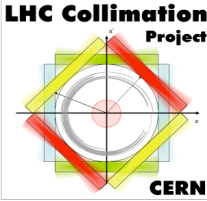
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Snapshot of LSA TRIM with collimator settings



Trim Editor v2.15.1b

SPS

Supercycles

- FT-3CNGS_TESTER (resident)
- FT-MDLHC_L14400_V1 (resident)
- LHC25.92-450_L28800_TESTER (resident)
- FASTLHC_FT500_L8400_V6
- FASTLHC_LSS46_L16800v1
- FT-3CNGS-MD_V1
- FT-3CNGS_5MSTESTER
- FT-3CNGS_HWTTEST
- FT-3CNGS_V1
- FT-CNGS-MD_L21600_V2

ParticleTransfer

SPSRING

Cycle Beam Process

FASTLHC_FT500_L8400 (0->8400)

Select All

Parameter selection - SPSRING

System

- DAMPER_PHASE
- EXTR OCTUPOLES
- GENERATION
- LATTICE MEASUREMENT
- LHC COLLIMATORS
- MOMENTUM
- OCTUPOLES
- RF-Hadron200
- RF-Hadron800
- RF-IONS

CSSInterface.SPS.TEST/RequiredAbsolutePositi

Field

- left_downstream
- left_upstream
- right_downstream
- right_upstream

Hide Field(s)

Setting Part

Value

Trim

Trim Points

Abort Trim

Cancel Last Trim

Trim History

Send to HW

Trim Expert Params


Timebase:

SC Cycle

Console

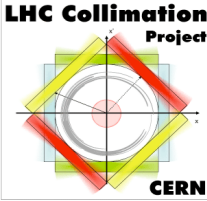
14:30:59 - Sorry, could not find any non null setting

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- FT-CNGS-MD_L21600_V2

ParticleTransfer

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FASTLHC_FT500_L8400 (0 -> 8400)

Select All

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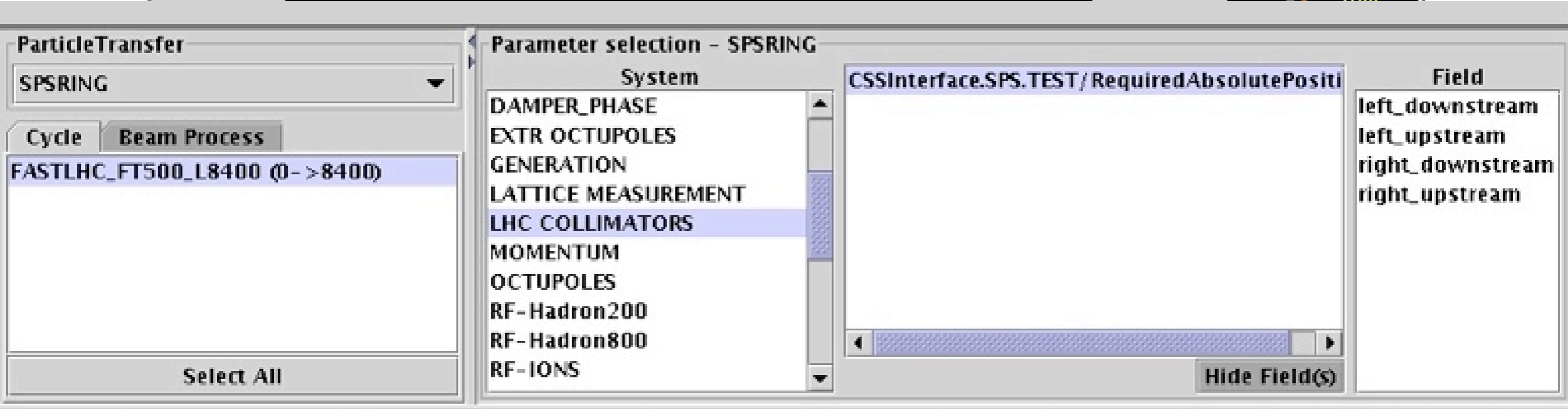
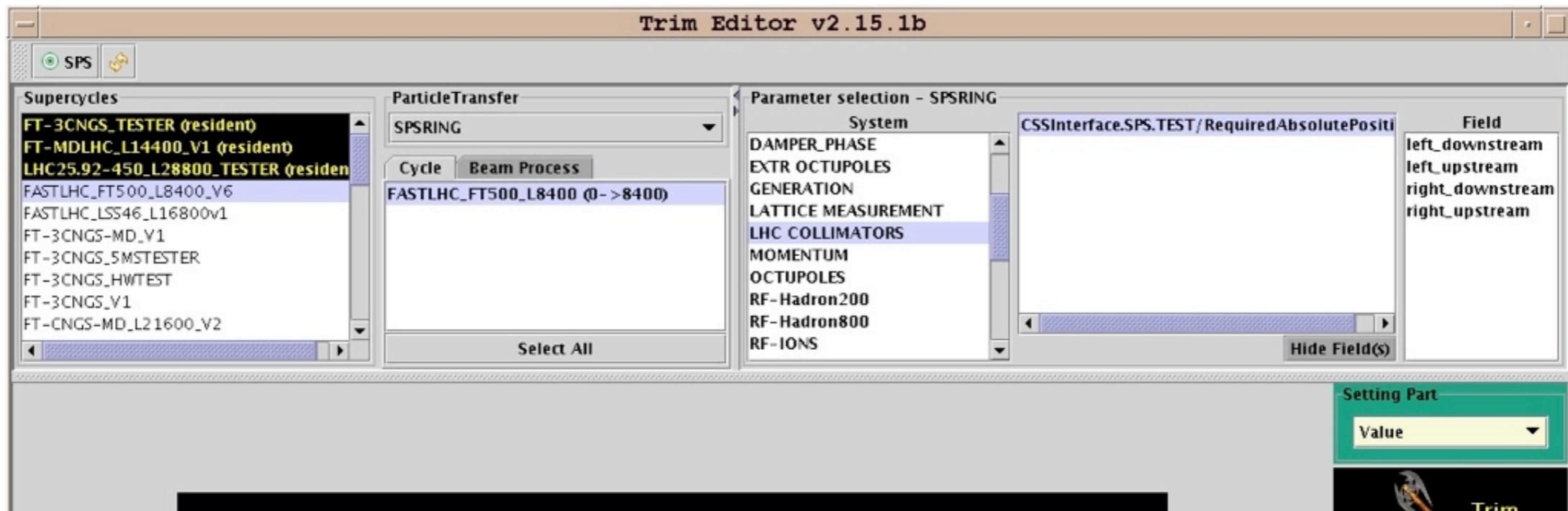
Timebase:

SC Cycle

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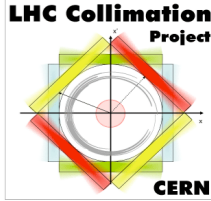
As it is now, the (discrete) TRIM interface is NOT adequate for the collimator control!

Console
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Timebase: SC Cycle

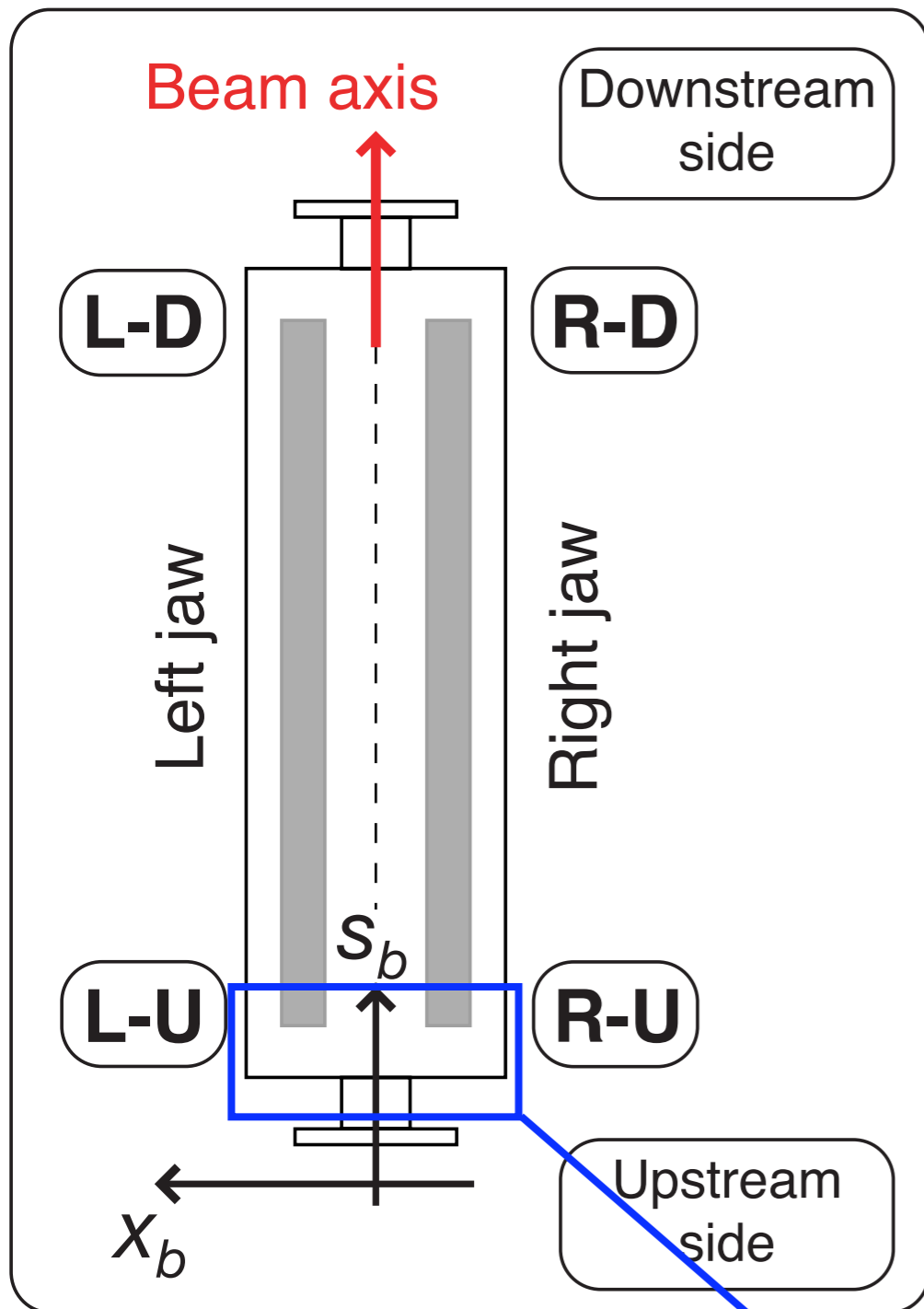


Overview of my talk

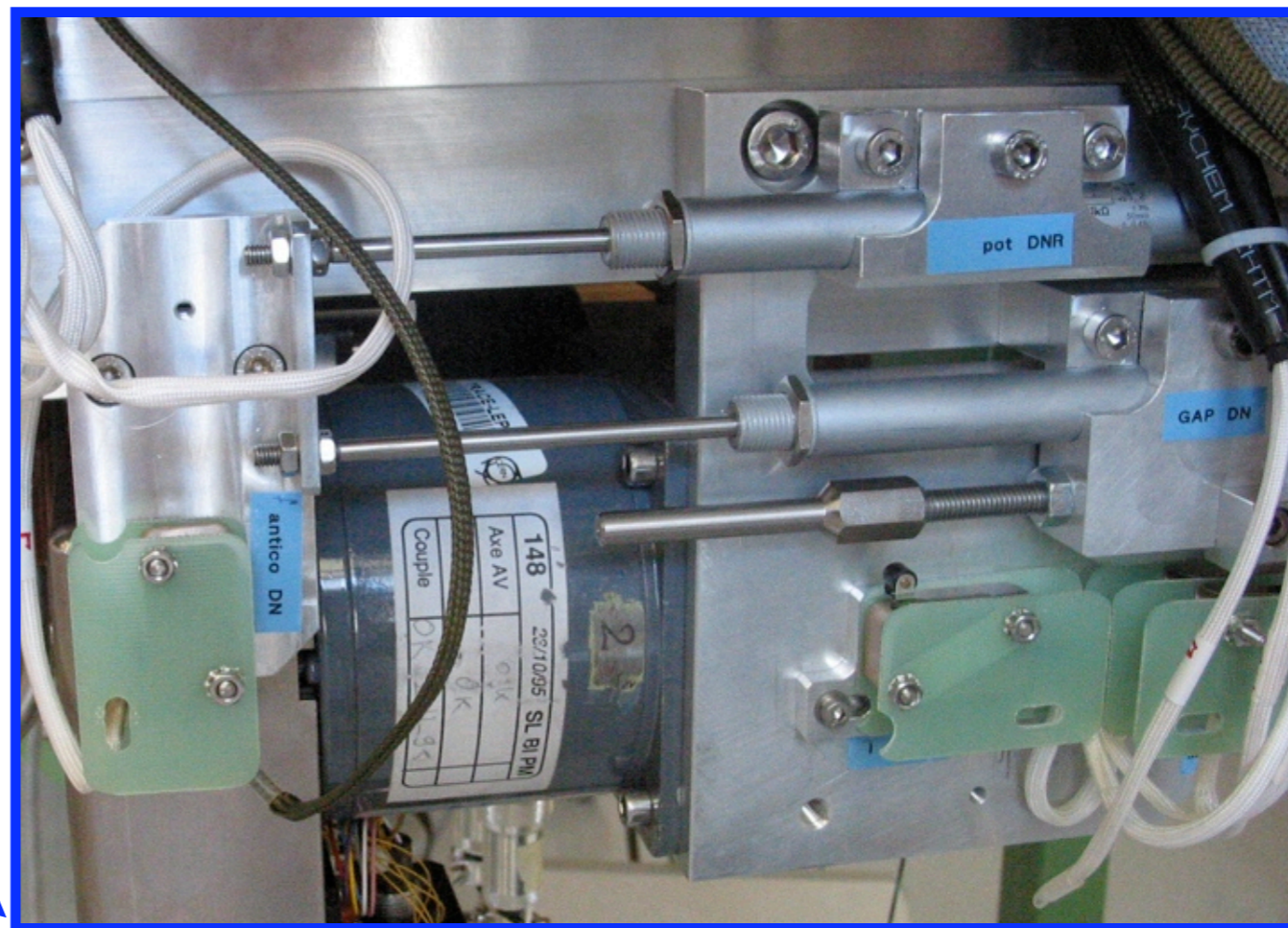


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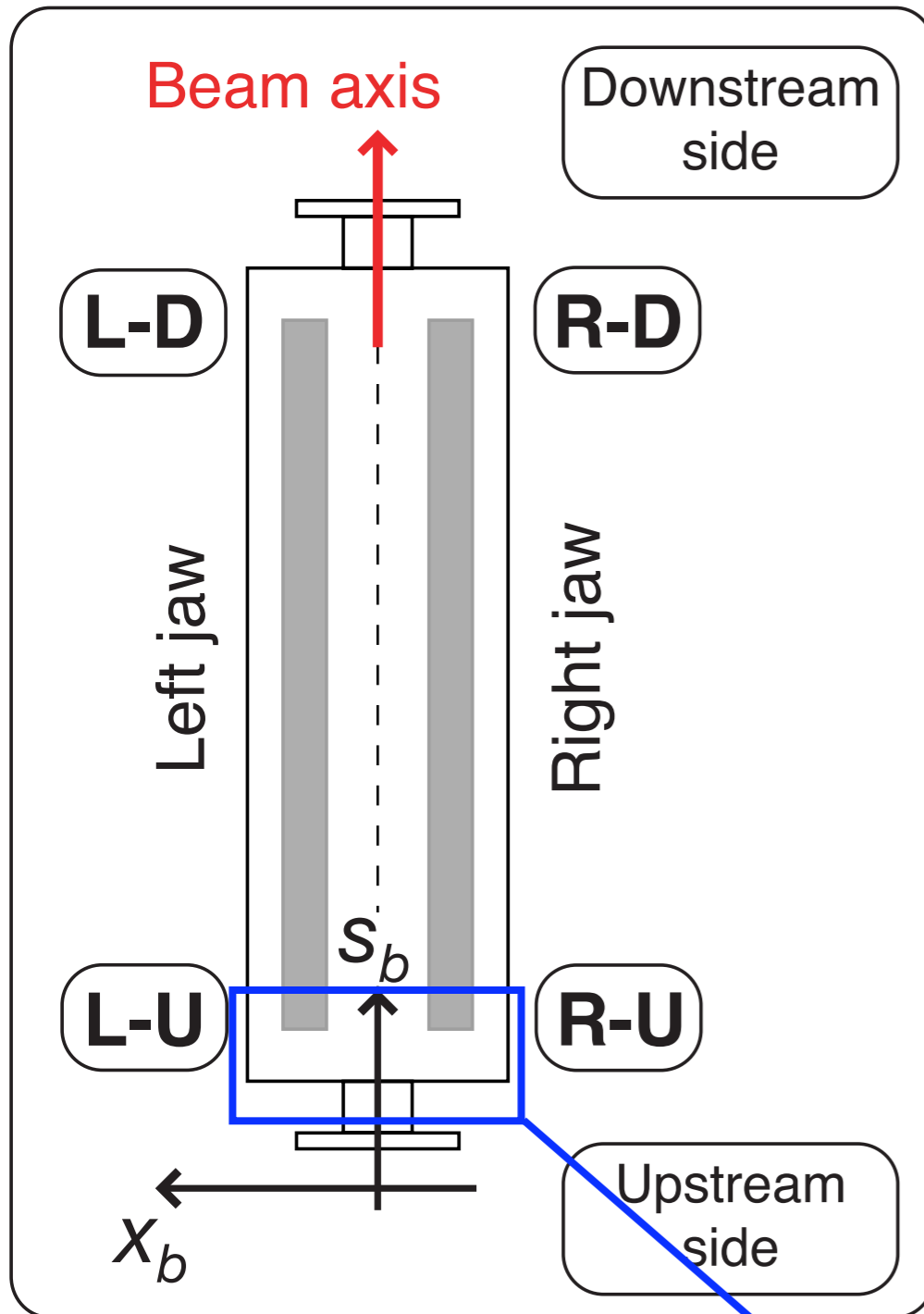
Reminder on collimator hardware



- 4 LEP stepping motors, one per jaw corner
- 4 resolvers count the motor steps
- 4 potentiometers measure the actual jaw position
- 2 LVDT's provide direct gap measurements
- 10 switches prevent breaking the mechanics
- Switch positions are our *absolute reference*

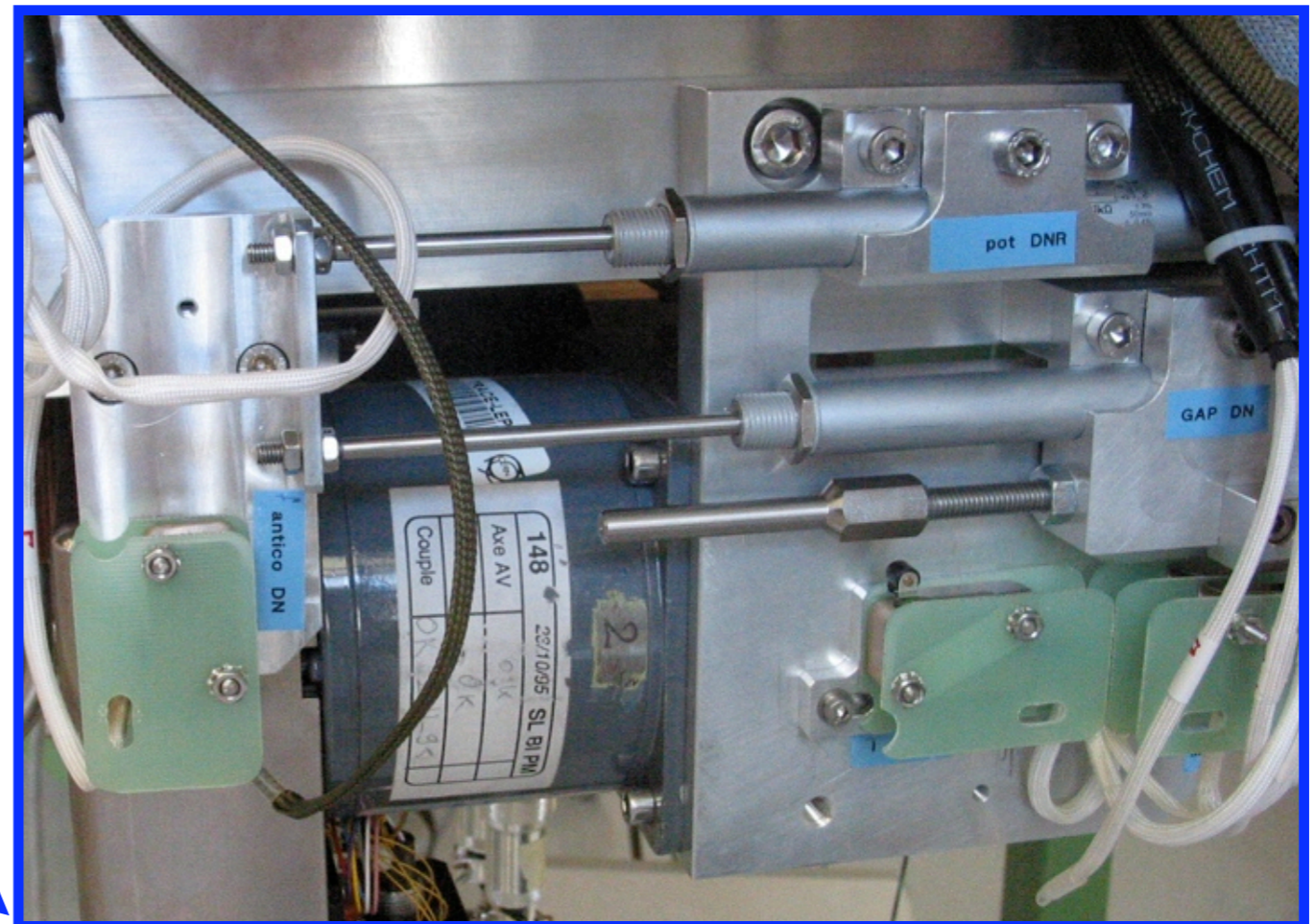


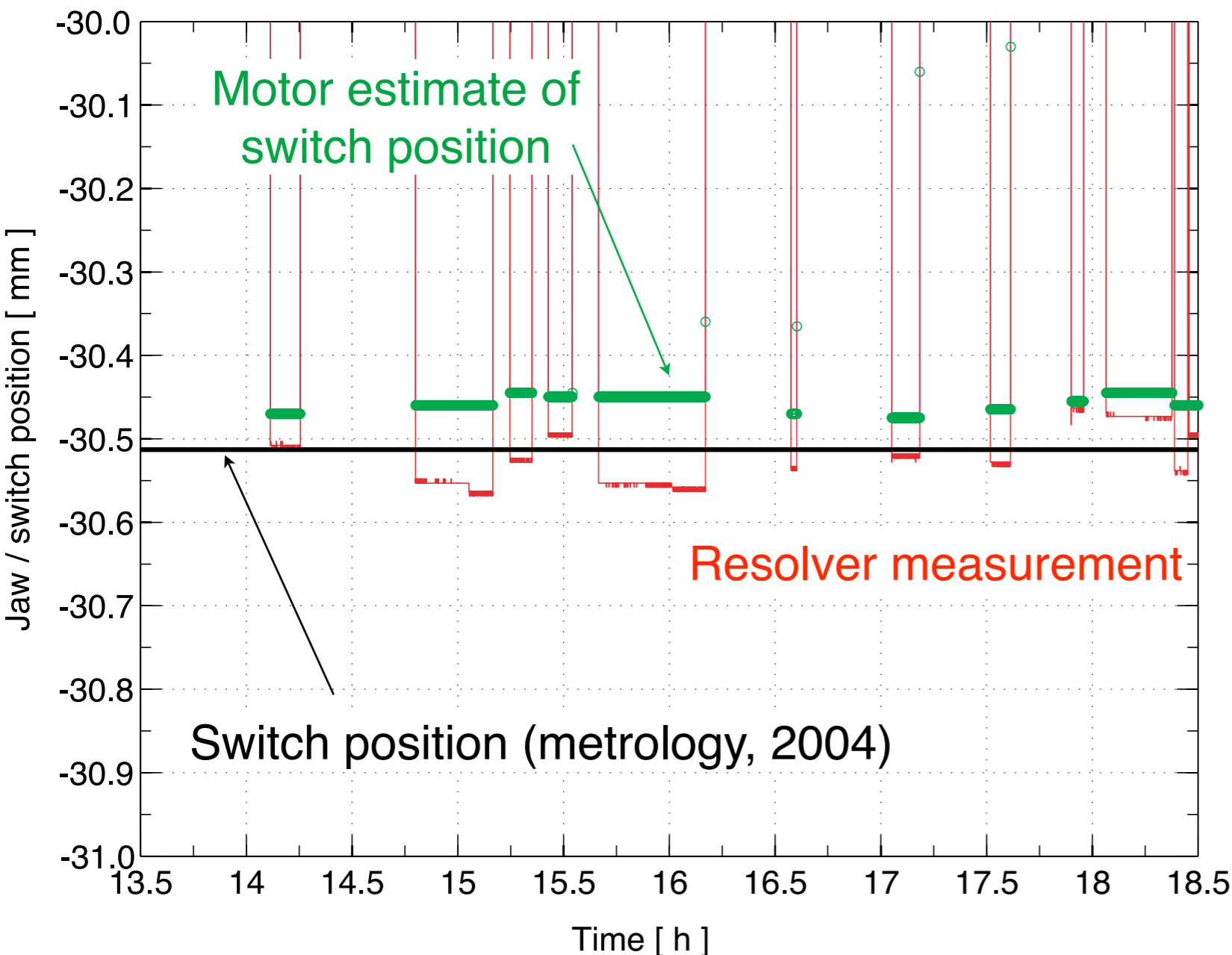
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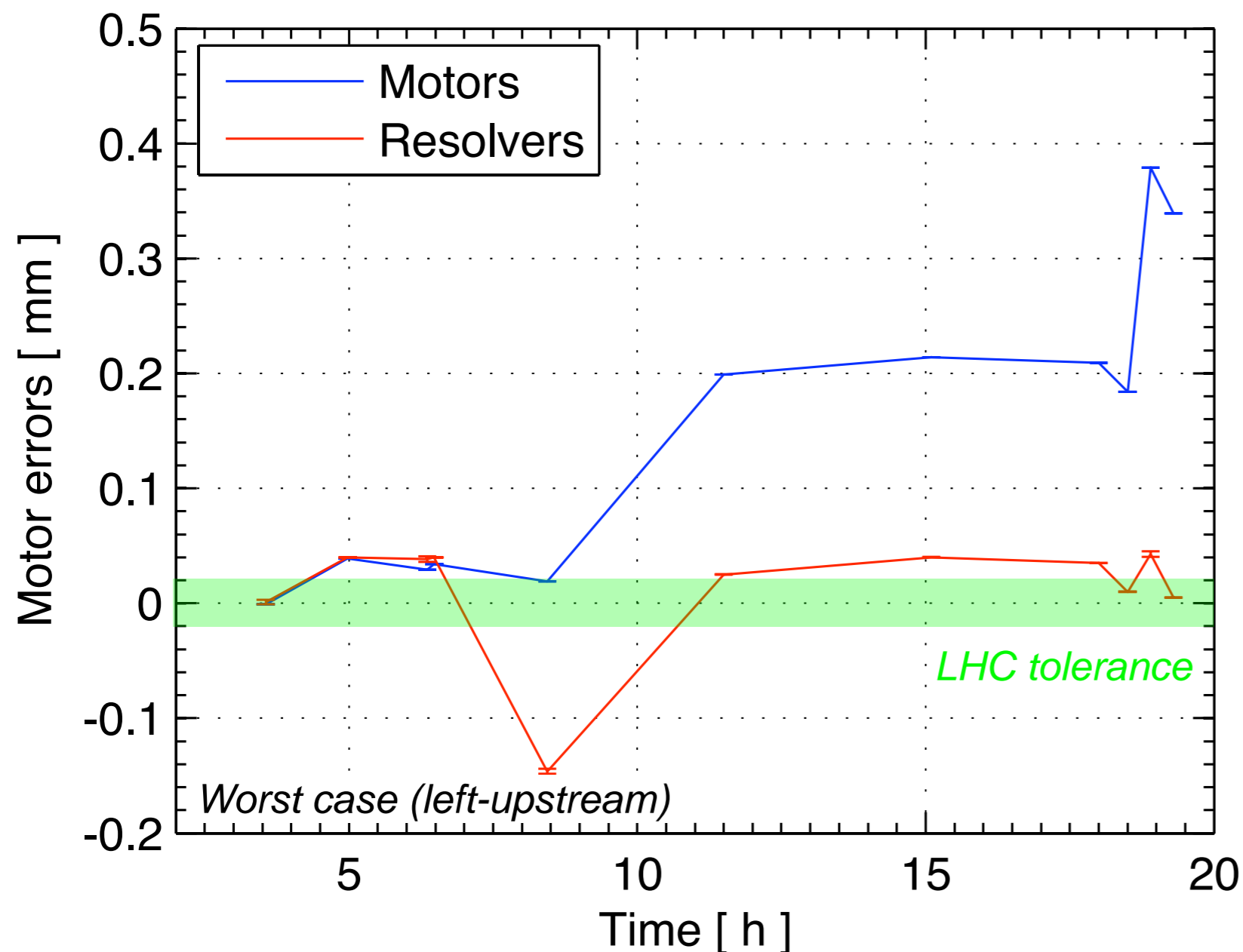
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Different than the LHC hardware

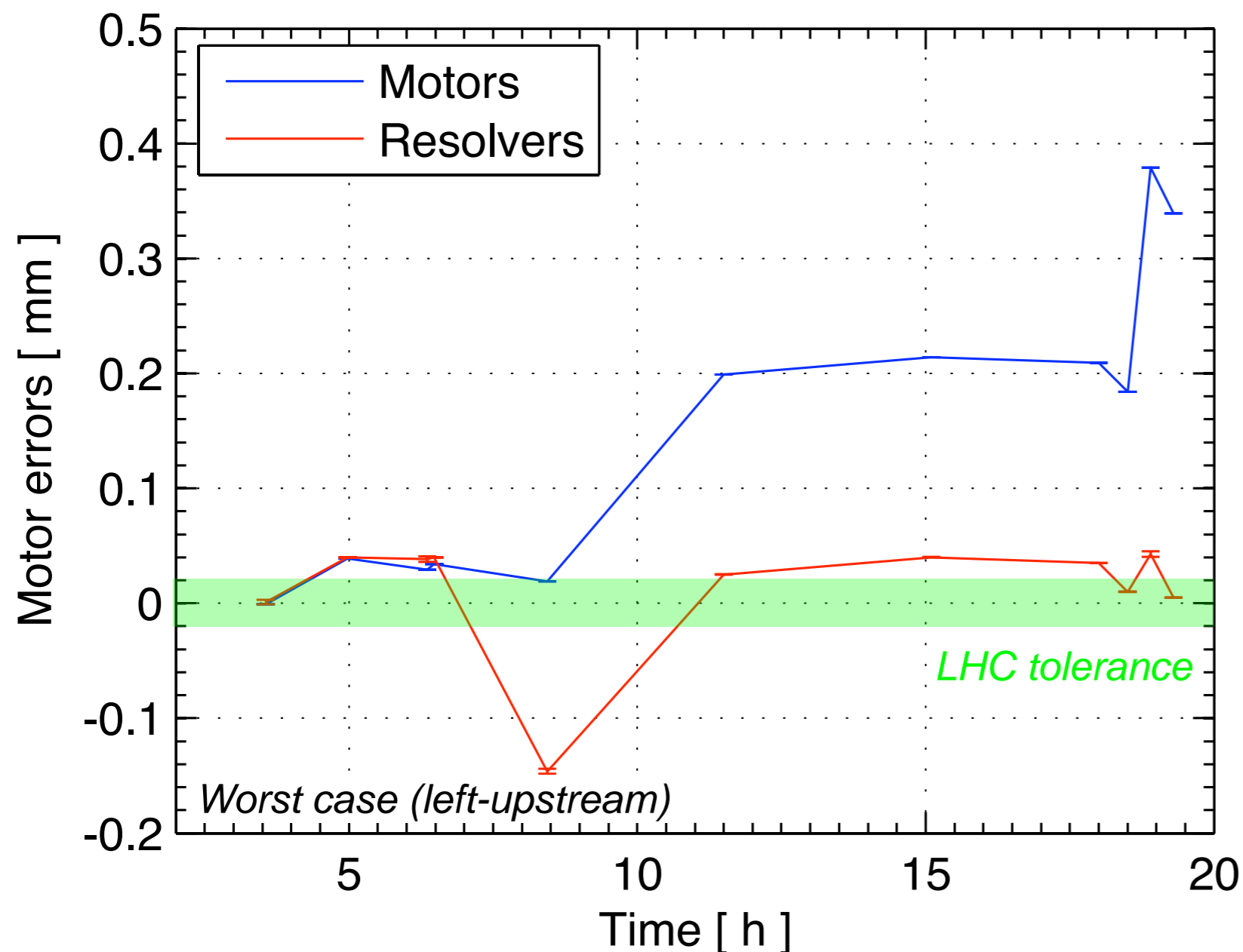




- Known to be a potential issue with LEP stepping motors.
The count of motor steps does not provide direct jaw measurements - errors add up!
- Total error up to hundreds of microns
- This caused problems during the MD: demanded settings were not up to date!
- SPS data can be corrected off-line for detailed studies



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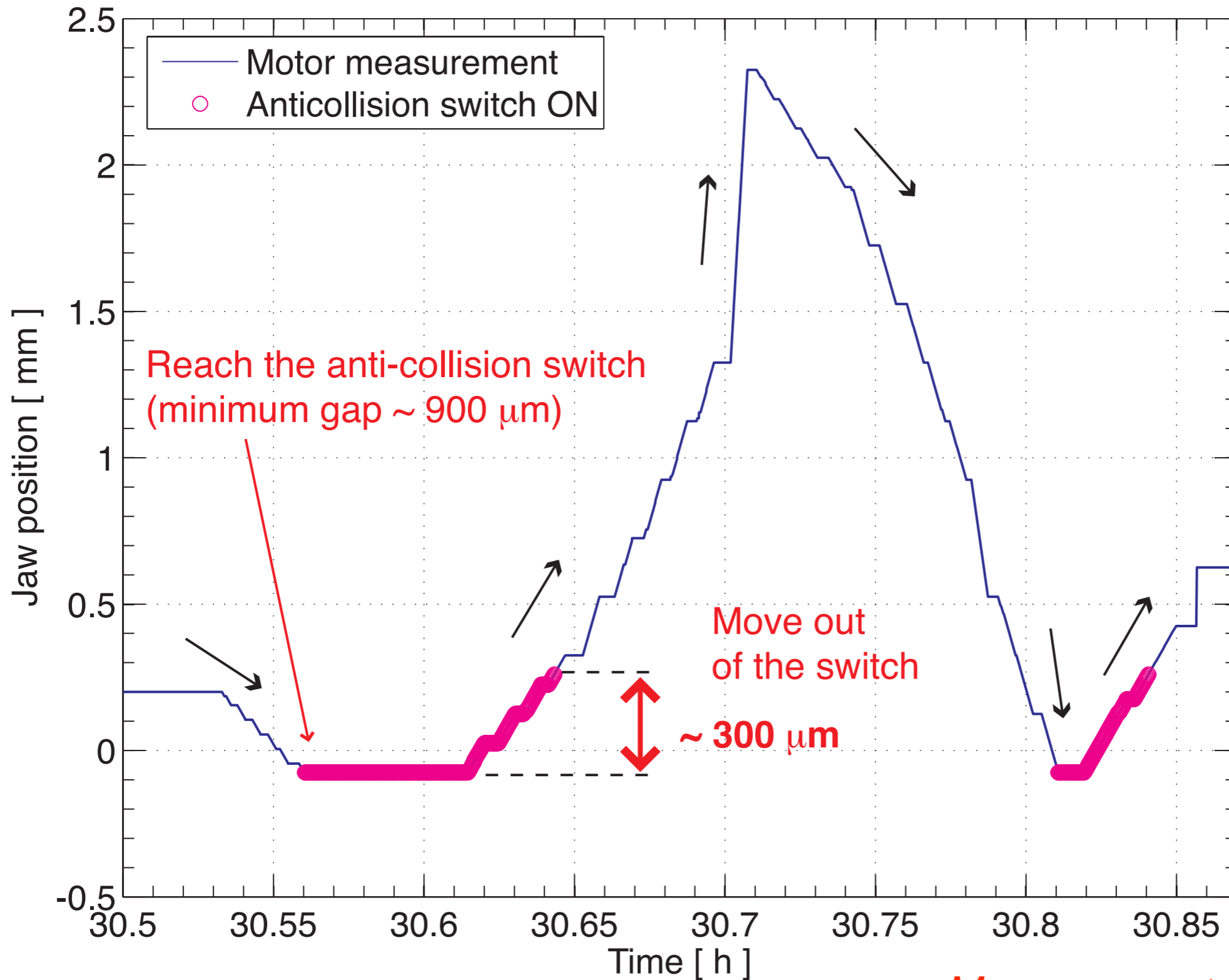


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The count of motor steps does not provide direct jaw measurements - errors add up!
- Total error up to hundreds of microns
- This caused problems during the MD: demanded settings were not up to date!
- SPS data can be corrected off-line for detailed studies

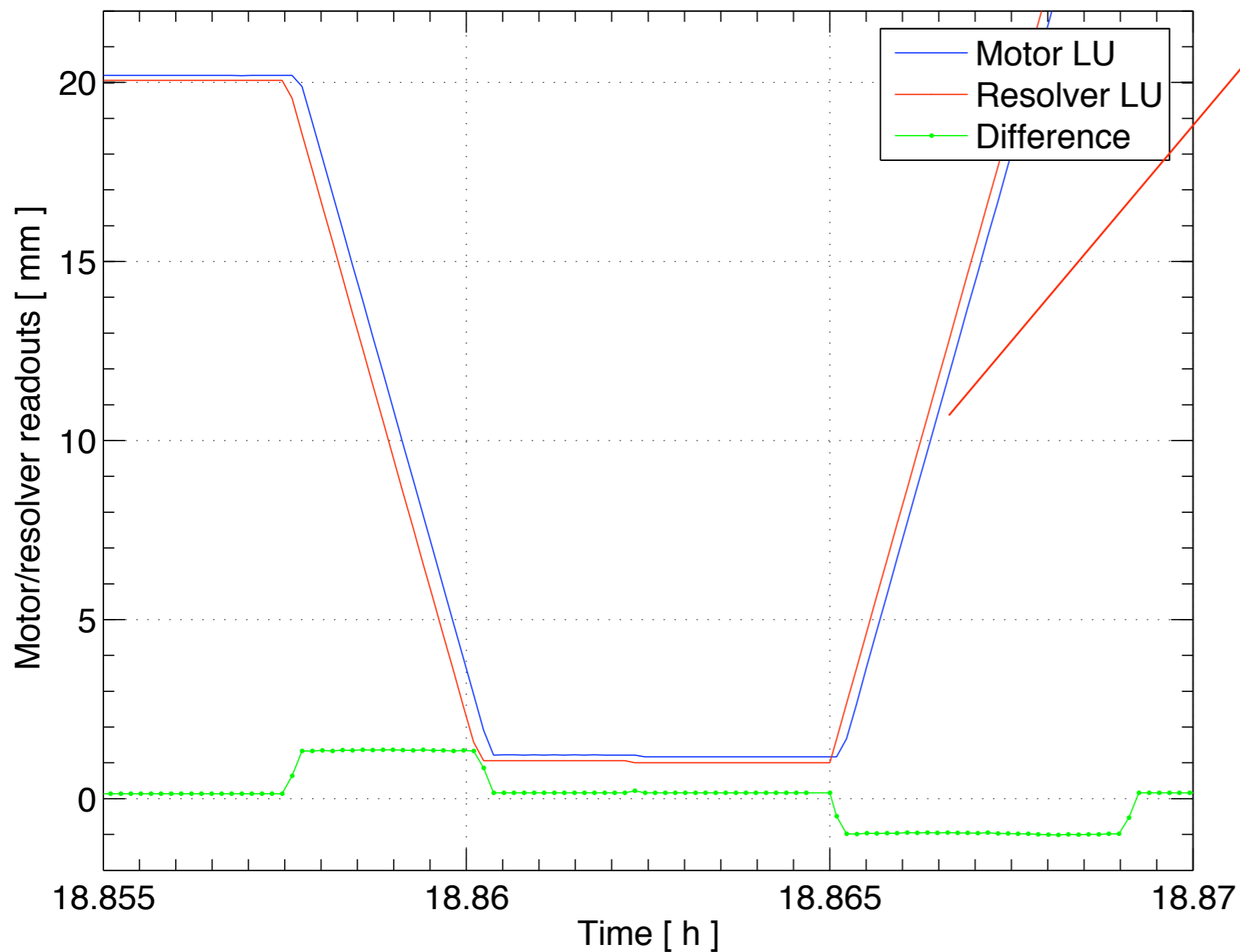
Solutions for the LHC are implemented:

- New motors are much better! (proven by TT40 test, see Roberto's talk)
- Precise position sensors will detect the error. *Do not rely on switch positions*
- In the software: implement the feature of "update motor settings"

Switch performance



Measurements only possible thanks to the new controls! We did not see that feature in 2004!



We measured delays of ~1s delay between different sensors!

1 s at 2mm/s: 2mm

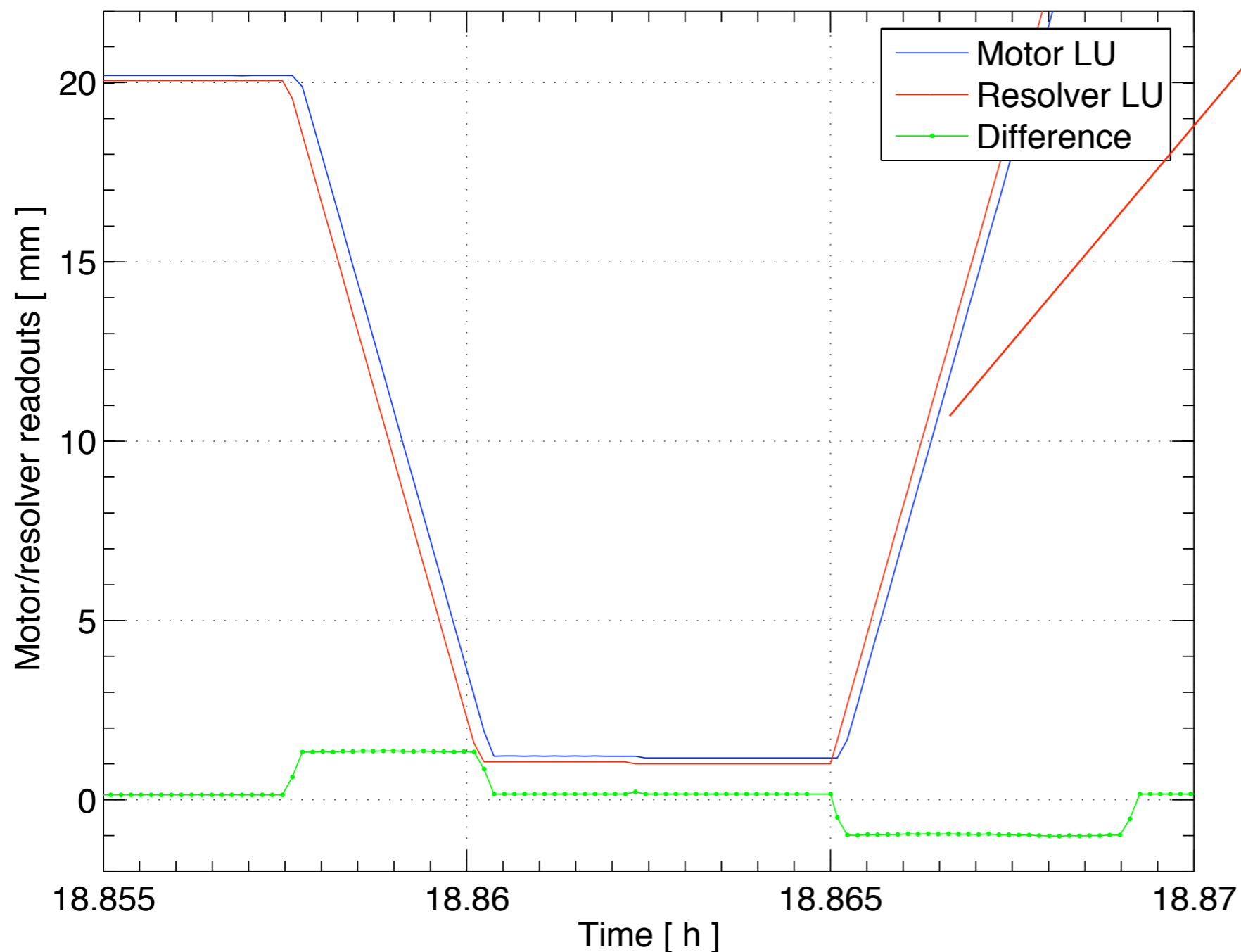
LHC tolerance: 20 μm

Time shift changes in time!

Not a big issue for the BB alignment on-line.

*Source of these delays needs to be **understood!***

Time delays between acquisitions



We measured delays of ~1s delay between different sensors!

*1 s at 2mm/s: **2mm***

*LHC tolerance: **20 μm***

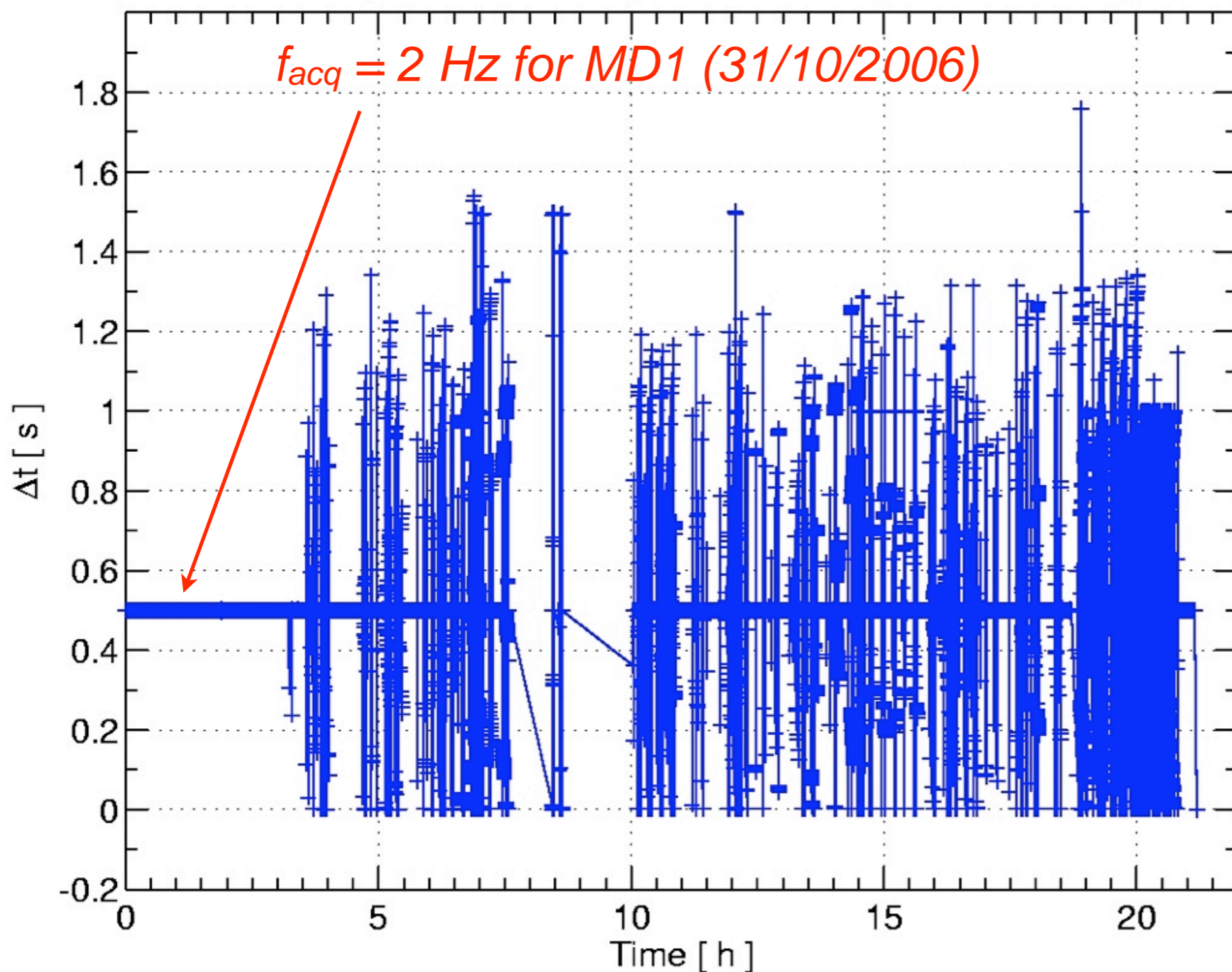
Time shift changes in time!

Not a big issue for the BB alignment on-line.

*Source of these delays needs to be **understood!***

Solution for the LHC:

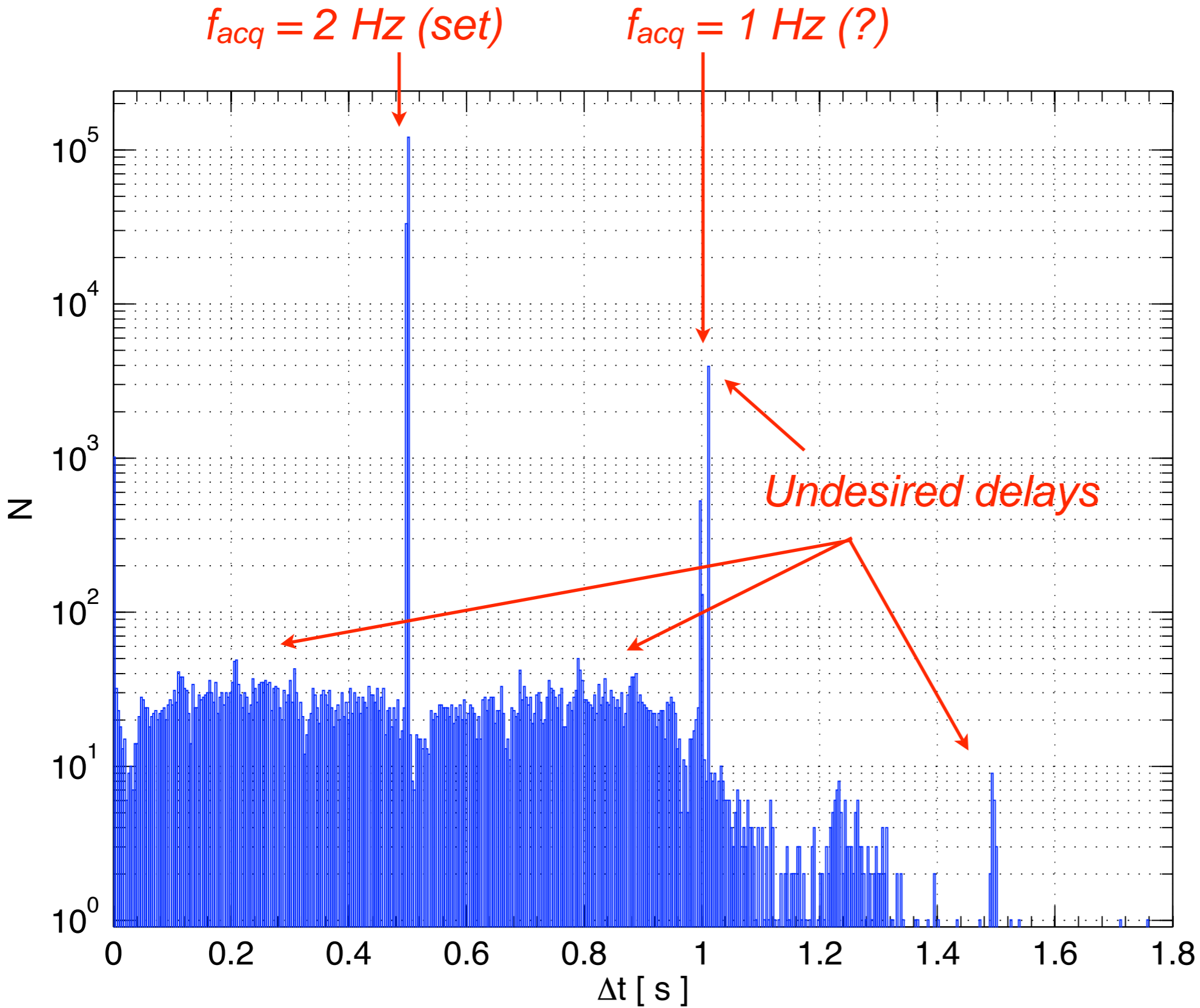
- Time-stamp the data at the low-level!



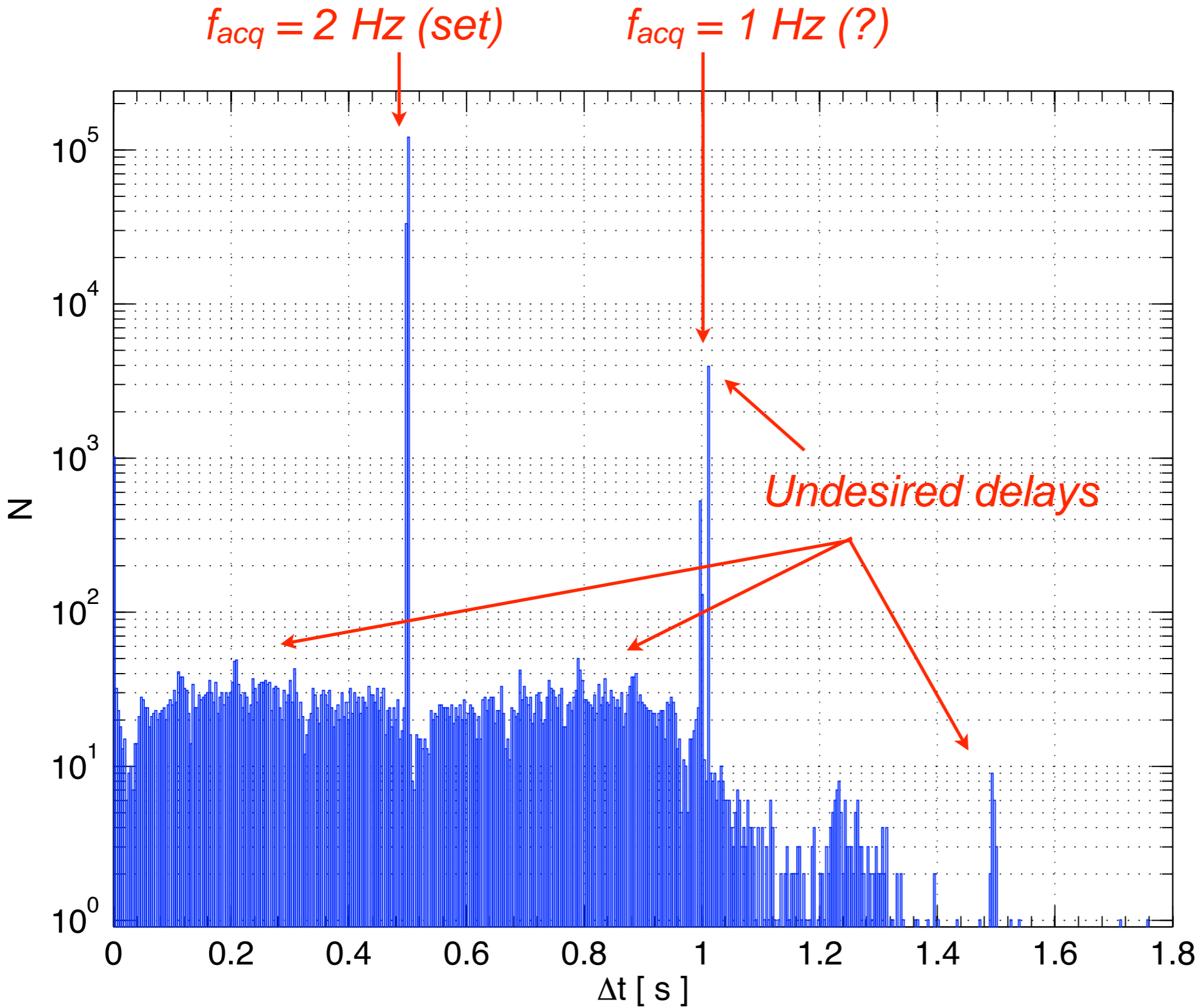
*During the MD, the position measurements published by the middle-level showed **time shifts** with respect to the nominal f_{acq}*

*We found a correlation with the **frequency of setting requests** (work load of middle level)!*

Statistics of time delays



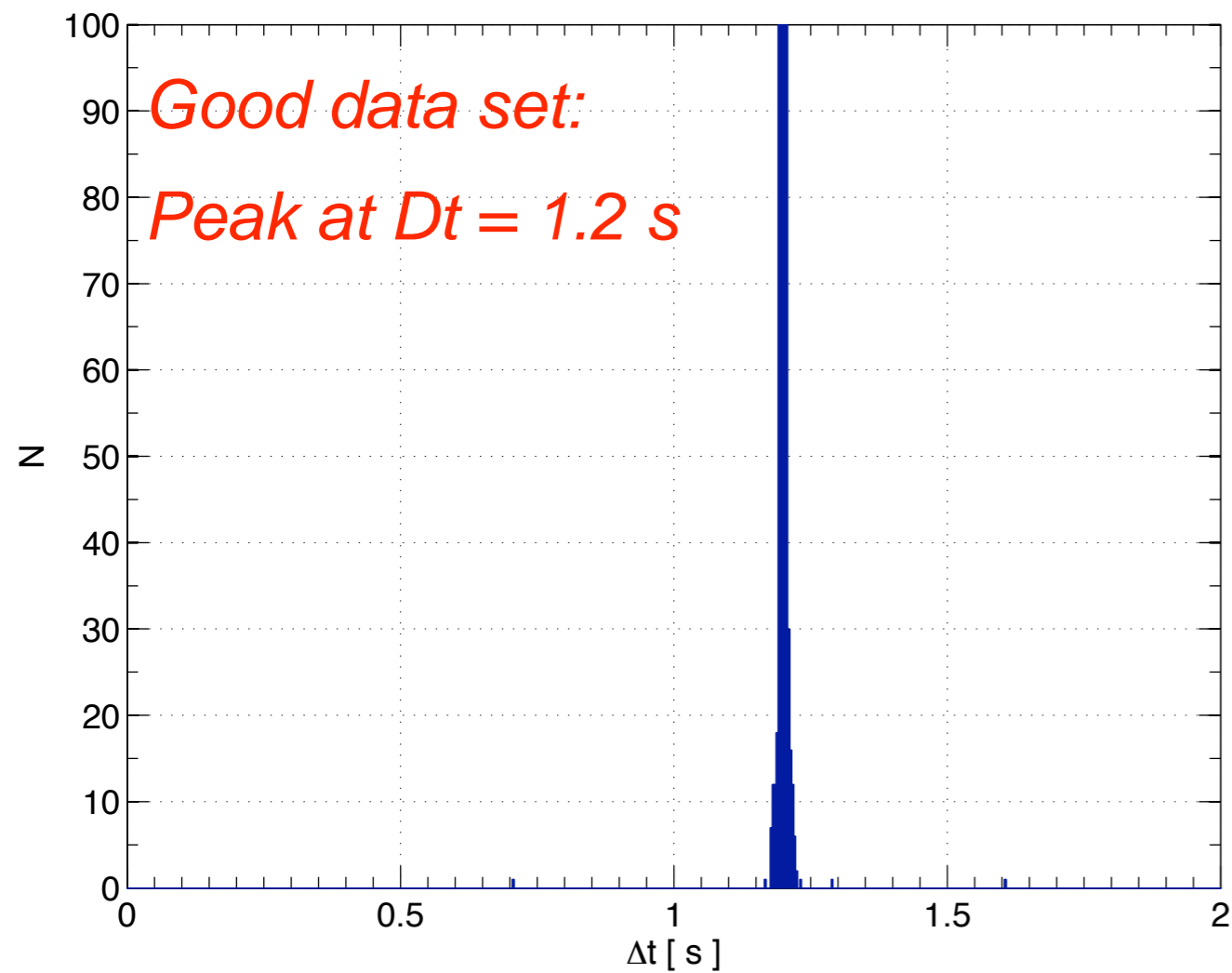
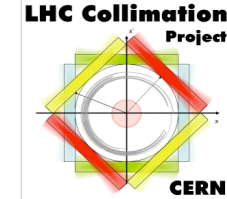
Statistics of time delays



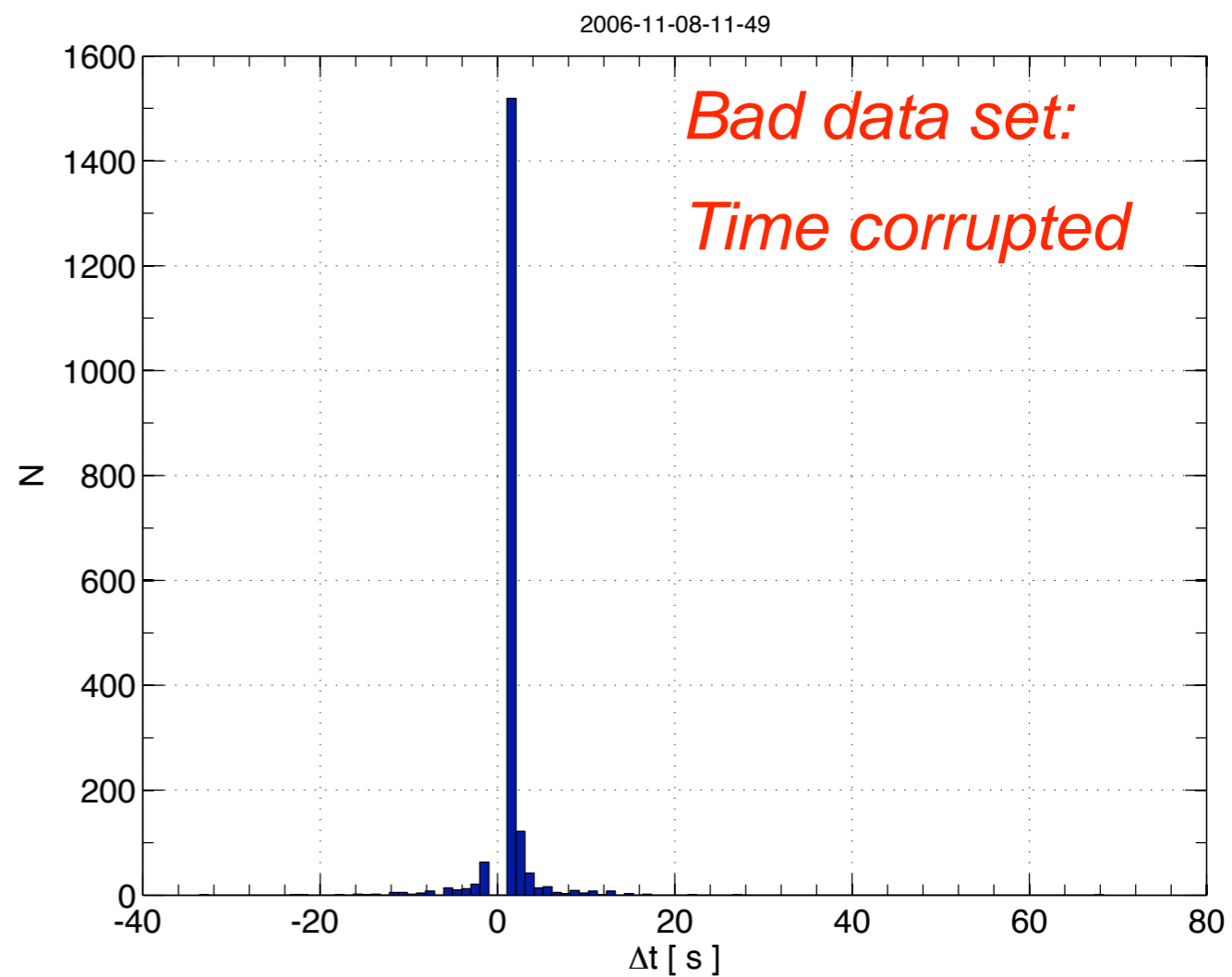
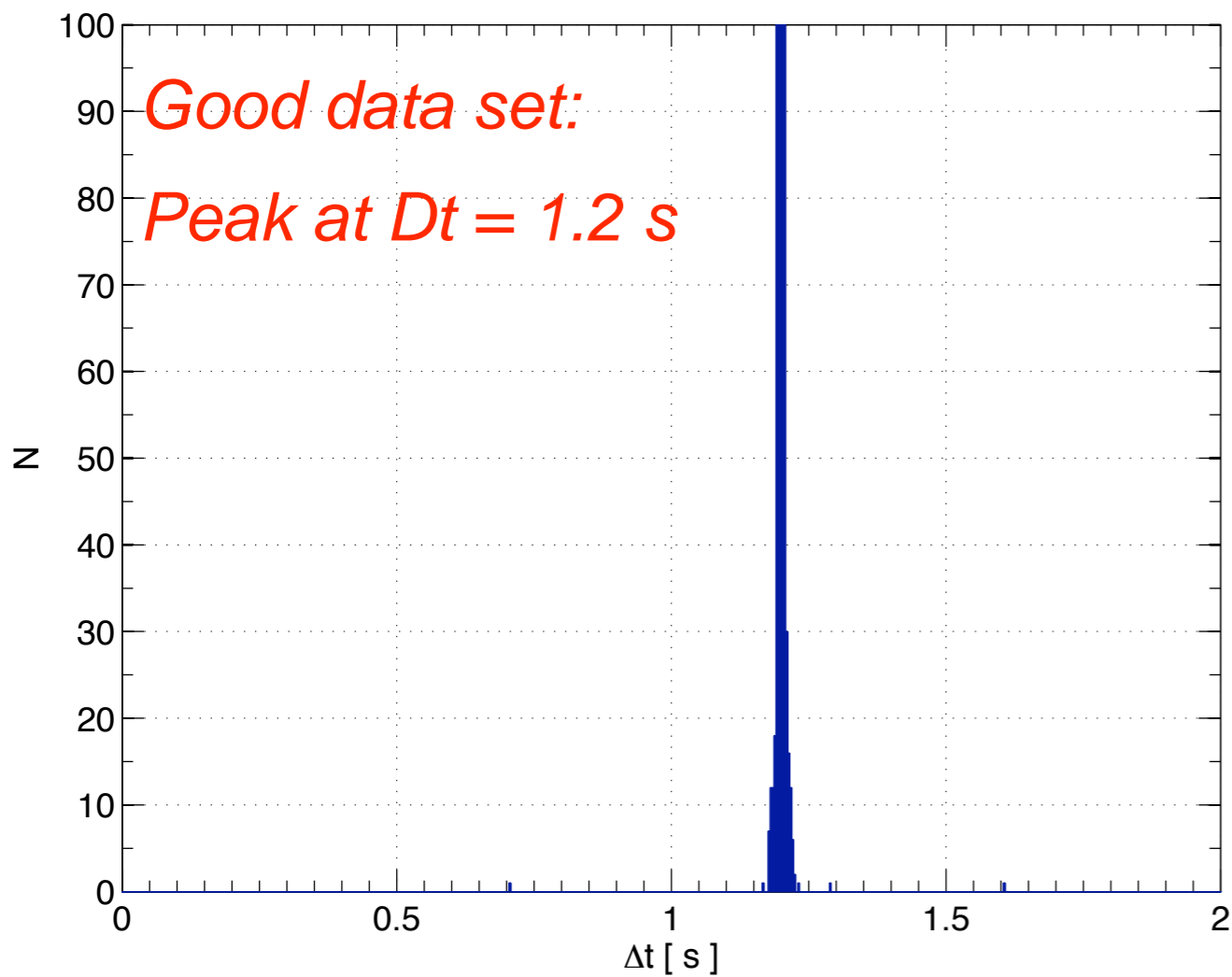
*No significant impact
on the on-line display
for BB alignment
Need to be improved
detailed off-line
studies at the LHC!!*



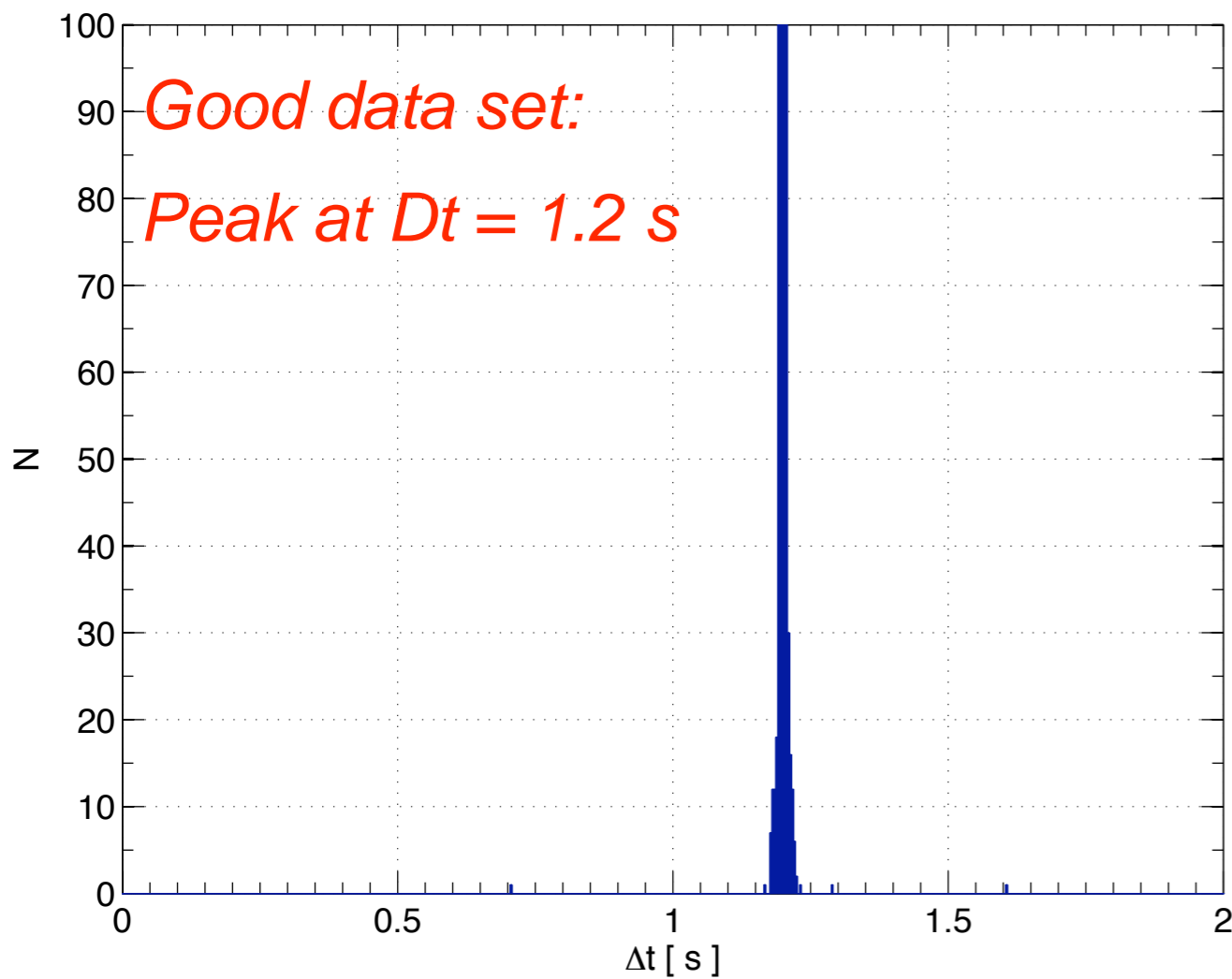
Time delays for BLM acquisition



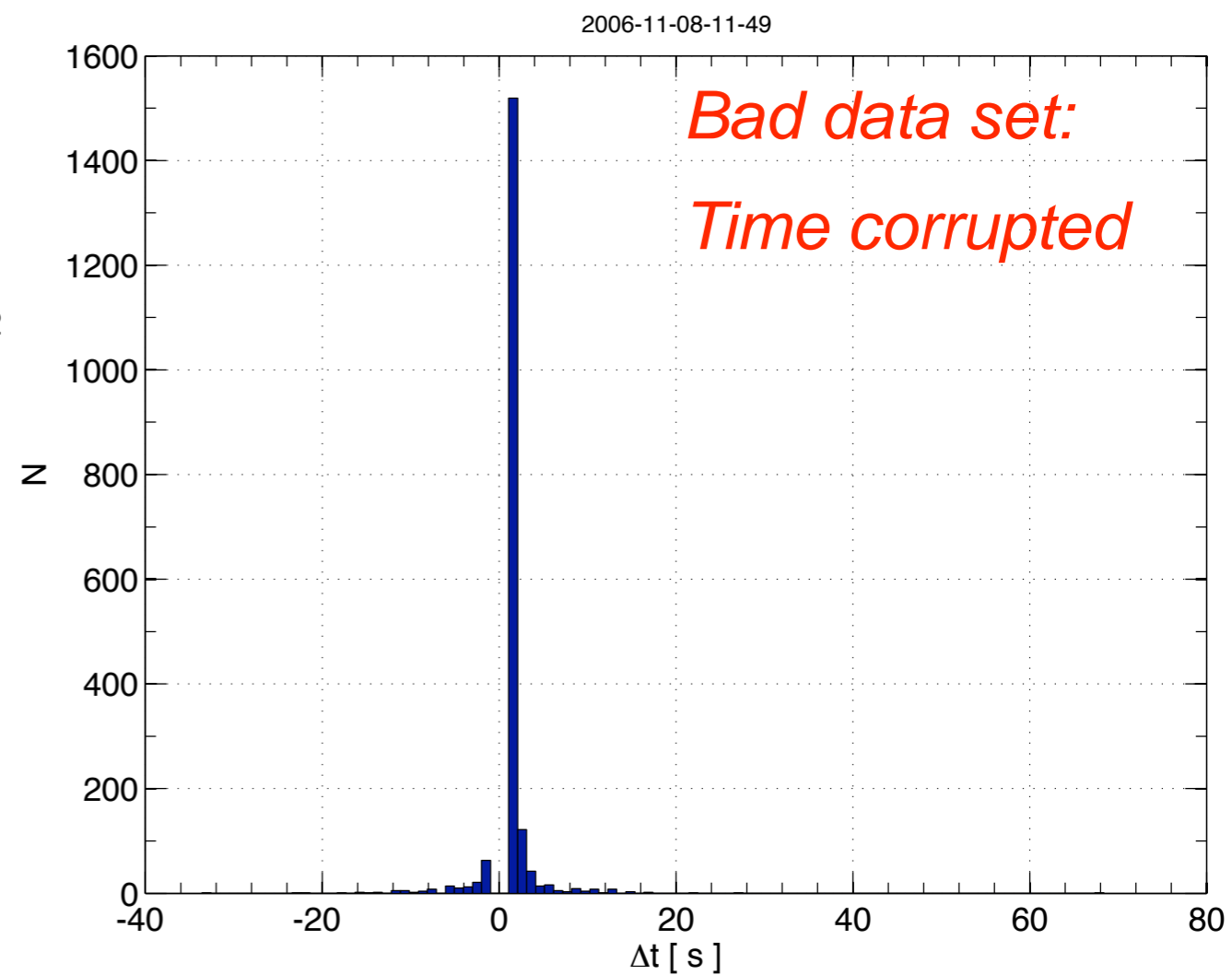
Time delays for BLM acquisition



Time delays for BLM acquisition

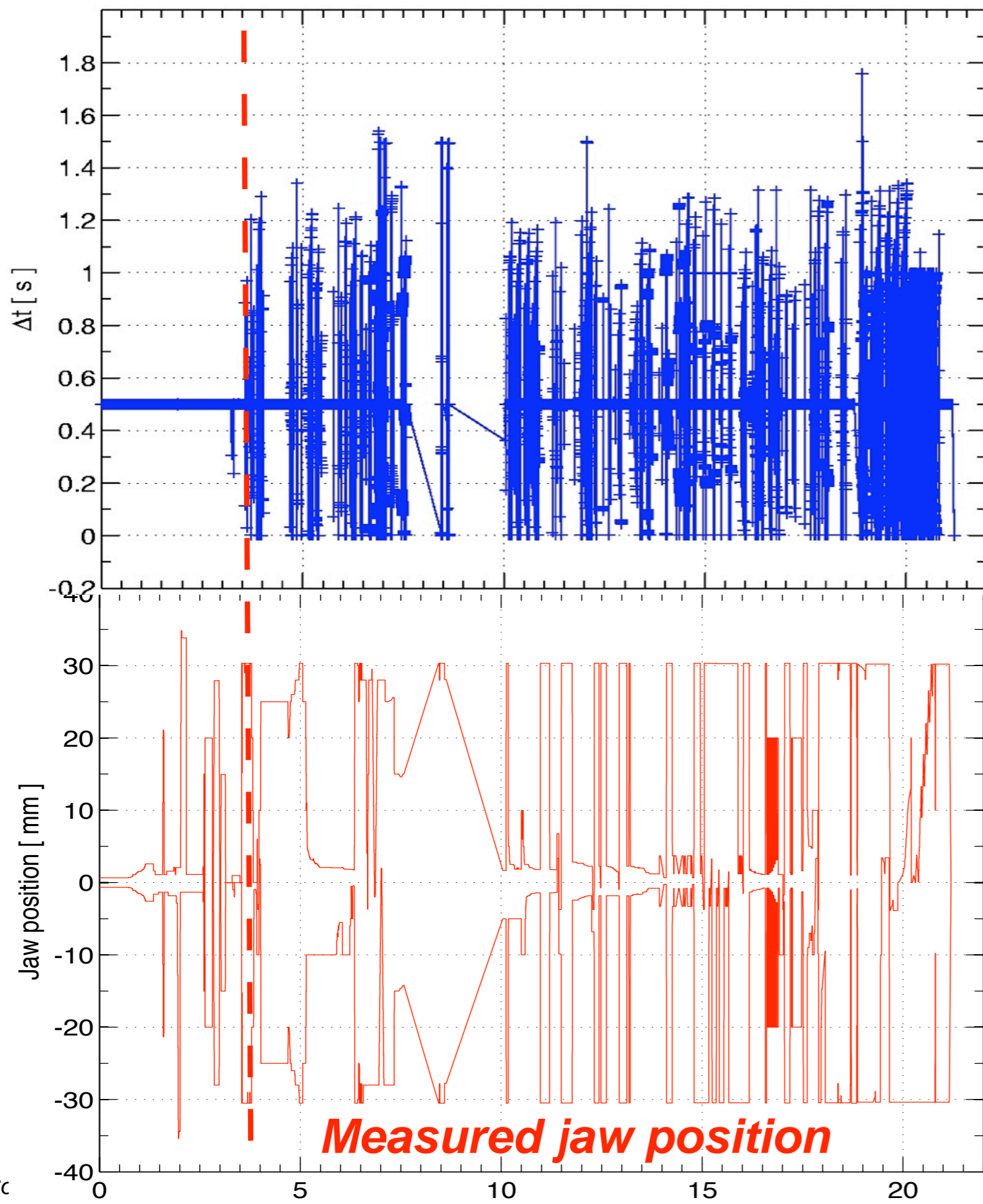


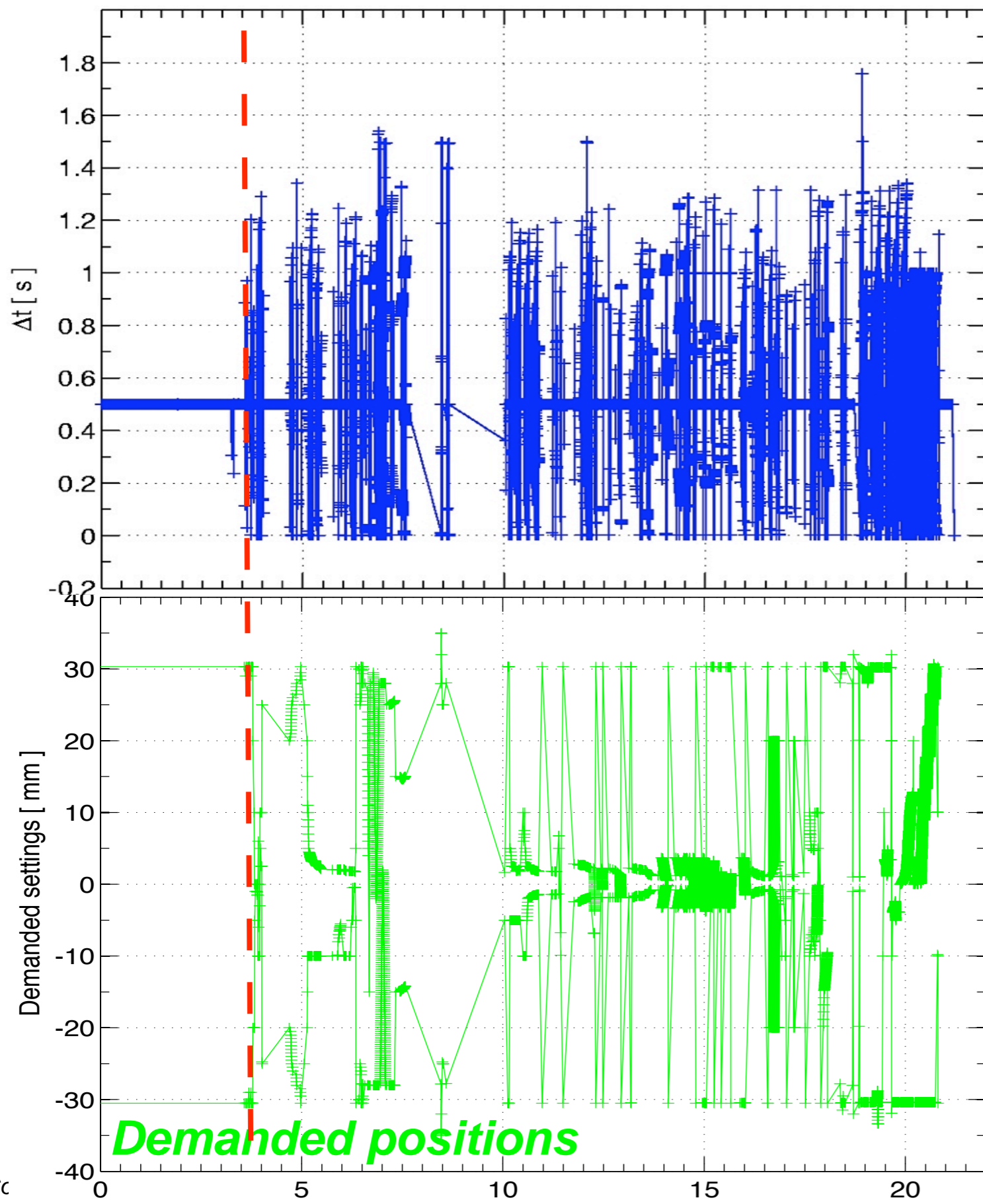
*Problem under
investigation with
BLM team . . .*

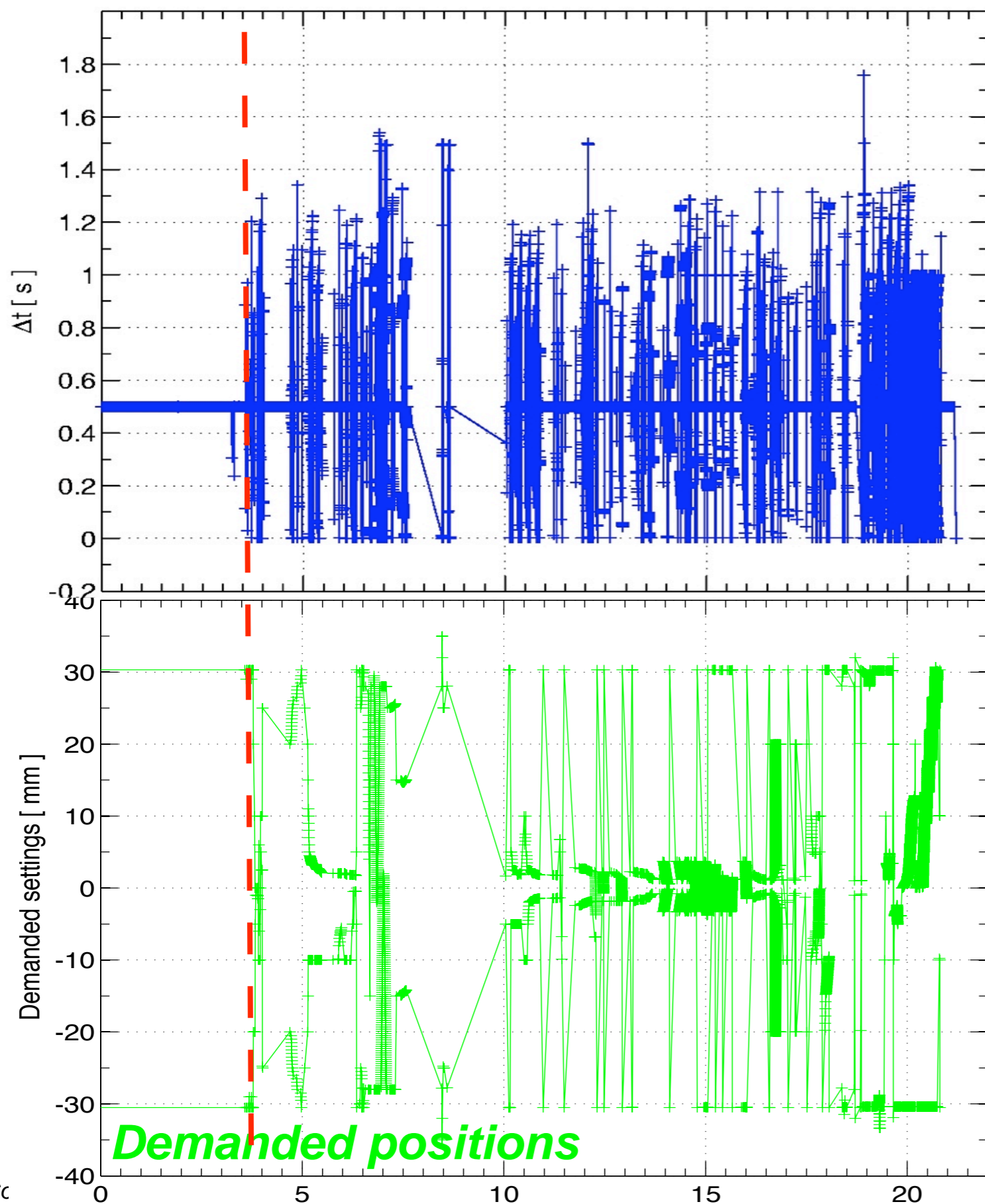


- ✓ SPS beam tests provided important validation of our critical choices for the collimator top-level control
 - First successful implementation within FESA/LSA environments*
- ✓ **Single collimator control** basically **ready for the LHC**
- ✓ Collimator control through the **LSA-TRIM** also tested. Further investigations of its performance are required
- ✓ We collected a significant amount of **useful data**
- ✓ What we have learnt:
 - Performance limitations of top level - under investigation*
 - Synchronization/timing can be an issue, mainly for off-line analyses*
- ✓ The hardware of SPS prototype not good enough for LHC
 - Motor settings lost accuracy and we could not easily detect it*
- ✓ Setup a **test bench** with final hardware for systematic performance checks within final controls environment

Delay between consecutive acquisitions

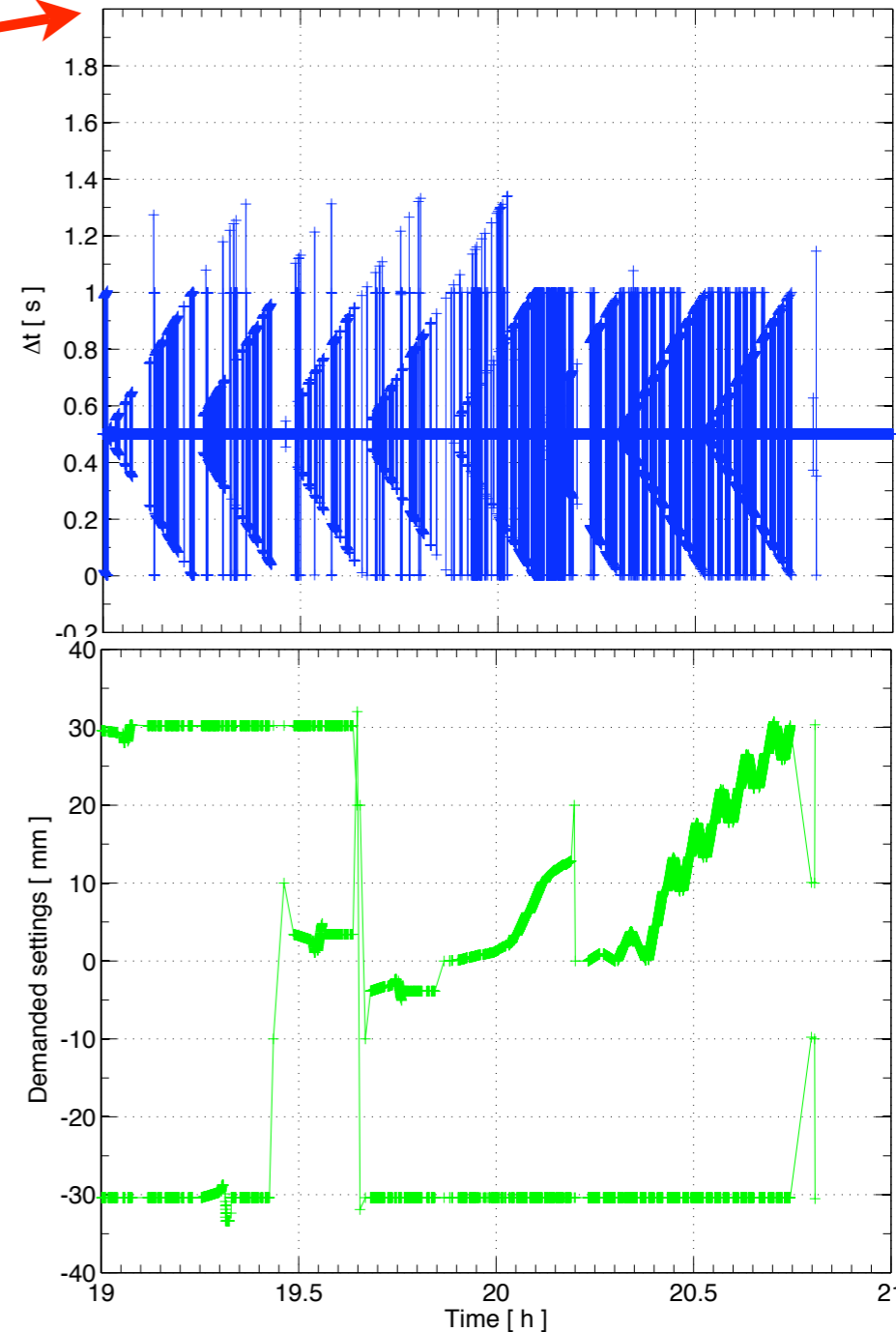
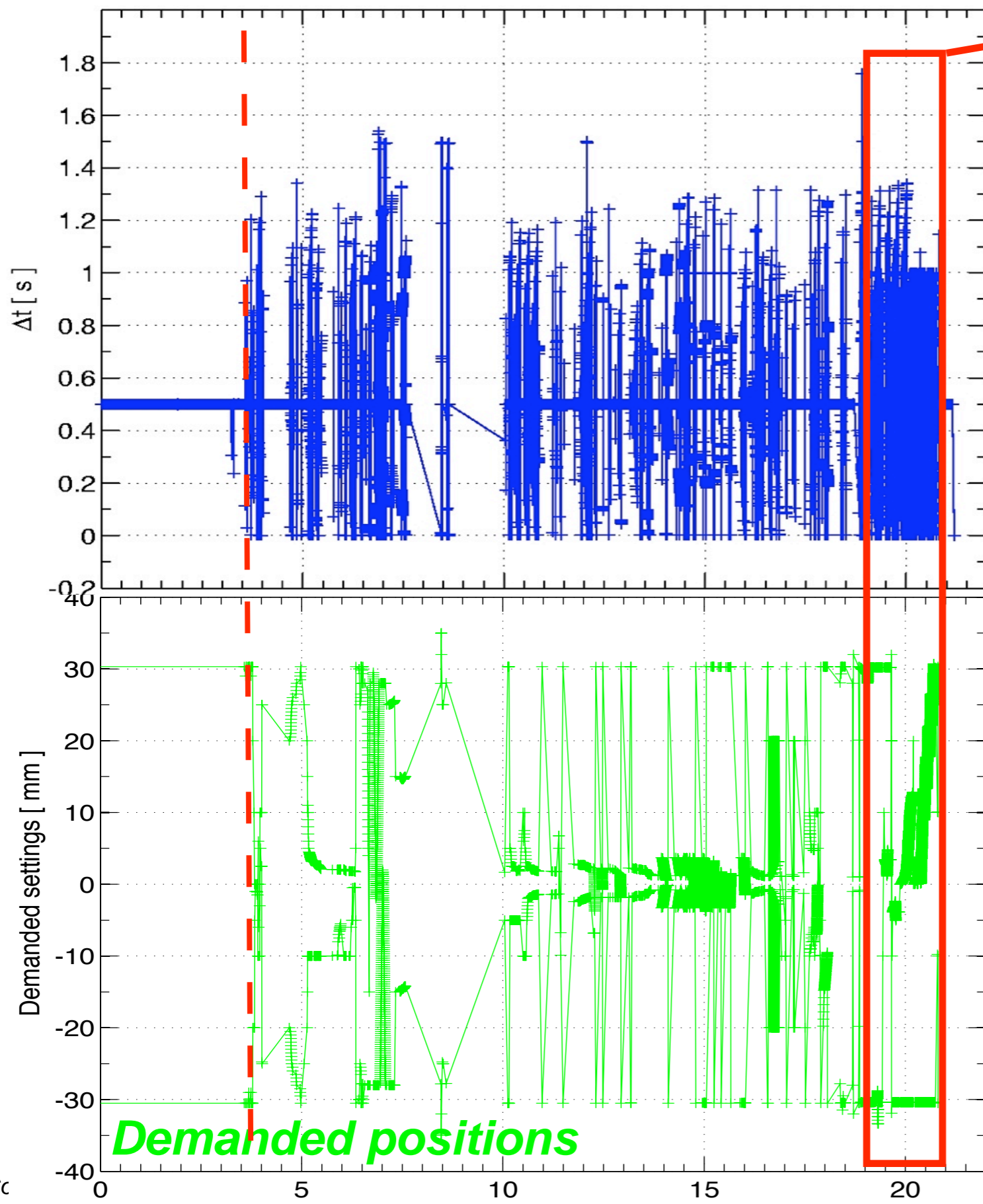






Different time response is induced by the work-load on the low level to process the demanded positions!

Delay between consecutive acquisitions





Time issues

